

[illegible][illegible]

```
DDDDDDDD  EEEEEEEEE  LL      TTTTTTTTTT  AAAAAA
DDDDDDDD  EEEEEEEEE  LL      TTTTTTTTTT  AAAAAA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DD      DD  EE      LL      TT      AA      AA
DDDDDDDD  EEEEEEEEE  LLLLLLLLLL
DDDDDDDD  EEEEEEEEE  LLLLLLLLLL
```

```
LL      SSSSSSSS
LL      SSSSSSSS
LL      SS
LL      SS
LL      SS
LL      SS
LL      SSSSSS
LL      SSSSSS
LL      SS
LL      SS
LL      SS
LL      SS
LLLLLLLLLL  SSSSSSSS
LLLLLLLLLL  SSSSSSSS
```

(1)	51	HISTORY	: DETAILED
(1)	130	DECLARATIONS	
(1)	378	PRIMARY COMMAND CHARACTER SWITCH	
(1)	419	PRIMARY COMMAND SCANNER	
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(1)	522	SLASH - OPEN CELL	
(1)	550	RETURN - CLOSE CURRENT OPEN CELL	
(1)	569	ENDFIELD - TERMINATE CURRENT FIELD	
(1)	590	FETCH - OBTAIN DATA SPECIFIED	
(1)	632	NEXTDOT - INCREMENT CURRENT LOCATION	
(1)	649	OUTPUT - DISPLAY CONTENT	
(1)	656	LINE FEED - DISPLAY NEXT	
(1)	682	OUTINS - OUTPUT INSTRUCTION	
(1)	784	DETERMINE CLOSEST RELOCATION REGISTER	
(1)	816	OUTPUTA - OUTPUT ADDRESS	
(1)	959	GETCHAR - GET INPUT CHARACTER ROUTINE	
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(1)	1083	DISPLAY INSTRUCTION RANGE	
(1)	1102	EQUALS - DISPLAY VALUE	
(1)	1124	SEMI - SECONDARY COMMAND SET	
(1)	1155	LEFT BRACKET - MODE SELECTION	
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(1)	1194	STEPOVER - STEP OVER ROUTINE CALL	
(1)	1228	BRKPOINT - SET/CLEAR BREAKPOINTS	
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(1)	1306	SEMI-I, PC VALUE	
(1)	1400	REGISTER SAVE AND RESTORE	
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(1)	2005	HANDLER FOR DEBUG EXCEPTIONS	
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(1)	2183	SETWRT - SET PAGES WRITABLE	
(1)	2214	FETCHP - FETCH DATA FROM ANOTHER PROCESS	
(1)	2237	QGET - QUEUE AST TO GET DATA FROM ANOTHER PROCESS	
(2)	2281	FPBYTE - FETCH BYTE FROM PROCESS	
(2)	2301	DPBYTE - DEPOSIT BYTE TO PROCESS	
(2)	2310	FPWORD - FETCH WORD FROM PROCESS	
(2)	2330	DPWORD - DEPOSIT WORD TO PROCESS	
(2)	2339	FPLONG - FETCH LONG FROM PROCESS	
(2)	2359	DPLONG - DEPOSIT LONGWORD TO PROCESS	



```
00000001 0000 1 SW_PROCESS=1
0000 1 .IF DF,SW_PROCESS
0000 2 .TITLE DELTA - MULTIMODE PROCESS DEBUGGER
0000 3 .IFF
0000 4 .TITLE XDELTA - EXECUTIVE DEBUGGER
0000 5 .ENDC
0000 6 .IDENT 'V04-000'
0000 7
0000 8
0000 9
0000 10
0000 11 *****
0000 12 *
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0000 30 *
0000 31 *****
0000 32
0000 33 ++
0000 34 FACILITY: EXECUTIVE, DEBUGGING TOOLS
0000 35
0000 36 ABSTRACT:
0000 37 THIS MODULE PRODUCES TWO DIFFERENT DEBUGGERS DEPENDING ON THE SETTING
0000 38 OF THE ASSEMBLY SWITCH, SW_PROCESS. DELTA IS A MULTIMODE PROCESS
0000 39 DEBUGGER USING SYSTEM SERVICES WHILE XDELTA IS A STANDALONE EXEC
0000 40 DEBUGGING TOOL.
0000 41
0000 42 COMMAND SYNTAX IS IDENTICAL FOR BOTH VERSIONS EXCEPT FOR ENVIRONMENTAL
0000 43 DIFFERENCES. THE SYNTAX IS QUITE TERSE AND SOMEWHAT CRYPTIC AND
0000 44 IS DOCUMENTED IN THE "GUIDE TO WRITING AN I/O DRIVER".
0000 45
0000 46 ENVIRONMENT:
0000 47 DELTA - NORMAL PROCESS ENVIRONMENT, VARIOUS ACCESS MODES.
0000 48 XDELTA - STANDALONE, RESIDENT, KERNEL MODE, IPL=31
0000 49 BOTH VERSIONS MUST BE POSITION INDEPENDENT - BEWARE!
```

```
0000 51 .SBTTL HISTORY ; DETAILED
0000 52
0000 53 :
0000 54 : AUTHOR: R. MUSTVEDT CREATION DATE: 15-NOV-76
0000 55 :
0000 56 : MODIFIED BY:
0000 57 :
0000 58 : V03-016 WHM0001 Bill Matthews 18-Jul-1984
0000 59 : Call CON$GETCHAR and CON$PUTCHAR to do I/O to the console
0000 60 : terminal. Call CON$OWNCTY to allocate and CON$RELEASECTY to
0000 61 : release the console terminal.
0000 62 :
0000 63 : V03-015 MSH0039 Michael S. Harvey 1-May-1984
0000 64 : Adjust image activation SET exception vector index
0000 65 : when setting up a DELTA rundown vector so that it
0000 66 : won't be lost by a subsequent image activation prior
0000 67 : to actual rundown.
0000 68 :
0000 69 : V03-014 MSH0002 Michael S. Harvey 16-Jan-1984
0000 70 : Reenable AST delivery in EXIT command to ensure process
0000 71 : doesn't hang up when EXIT issued from kernel mode. Also,
0000 72 : lengthen input command buffer to match specified maximum
0000 73 : length in input QIO.
0000 74 :
0000 75 : V03-013 TCM0003 Trudy C. Matthews 13-Dec-1983
0000 76 : Use 'Write enable bit' when enabling and disabling
0000 77 : console terminal access for venus.
0000 78 :
0000 79 : V03-012 KDM0084 Kathleen D. Morse 27-Sep-1983
0000 80 : Add MicroVAX I support to CPUDISP macros.
0000 81 :
0000 82 : V03-011 RLRCPUISP Robert L. Rappaport 15-Jun-1983
0000 83 : Recode CPUDISP macros to use new format.
0000 84 :
0000 85 : V03-010 MIR1039 Michael I. Rosenblum 27-May-1983
0000 86 : Fix non PIC reference in New format QIO
0000 87 :
0000 88 : V03-009 MIR0039 Michael I. Rosenblum 29-Apr-1983
0000 89 : Make the process based DELTA use itemlist qio's with
0000 90 : The no editing bit set.
0000 91 :
0000 92 : V03-008 JLV0236 Jake VanNoy 25-MAR-1983
0000 93 : Make QIO a QIOW in OUTZ$STRING so that a read will
0000 94 : not block write.
0000 95 :
0000 96 : V03-007 TCM0002 Trudy C. Matthews 16-Feb-1983
0000 97 : Correct console enable mask in TCM0001.
0000 98 :
0000 99 : V03-006 ROW0159 Ralph O. Weber 28-JAN-1983
0000 100 : Enhance DELTA initialization to set all pages in DELTA to user
0000 101 : writable. This corrects a problem encountered while trying to
0000 102 : debug DCL with DELTA. It also guarantees that DELTA will work
0000 103 : in all access modes. Change limit on rundown handler vector
0000 104 : table from 505 to <256-7>.
0000 105 :
0000 106 : V03-005 TCM0001 Trudy C. Matthews 11-Jan-1983
0000 107 : Change 11/780 machine check handler to write PR$ SBIFS back
0000 : to itself to clear error bit. Add 11/790 machine check
```



0000 108 :  
0000 109 :  
0000 110 :  
0000 111 :  
0000 112 :  
0000 113 :  
0000 114 :  
0000 115 :  
0000 116 :  
0000 117 :  
0000 118 :  
0000 119 :  
0000 120 :  
0000 121 :  
0000 122 :  
0000 123 :  
0000 124 :  
0000 125 :  
0000 126 :  
0000 127 :  
0000 128 :

handler; initialize 11/790 console interface registers.

V03-004 ROW0143 Ralph O. Weber 24-NOV-1982  
Change process-mode OUTZSTRING to do single QIO for whole  
string. Make terminal read/write QIOs do a \$WAITEF and retry  
if insufficient resources error is returned by the QIO system  
service. Make reference to CTL\$GL\_USRUNDWN in SETRUNDWN a  
weak reference so that DELTA can be linked with SYSINIT.  
Fix numerous branch destinations broken by the above. Add  
call to \$IODEF definition macro.

V03-003 ACG0290 Andrew C. Goldstein, 5-May-1982 20:01  
Condition rundown handler on user mode startup

V03-002 ACG0286 Andrew C. Goldstein, 13-Apr-1982 15:12  
Use privileged rundown handler to reset exception vectors

V03-001 RIH0097 Richard I. Hustvedt 1-Apr-1982  
Turn off processor register mode when proceeding.

```
0000 130      .SBTTL DECLARATIONS
0000 131
0000 132
0000 133      : INCLUDE FILES:
0000 134      :
0000 135      $ACBDEF      : DEFINE AST CONTROL BLOCK
0000 136      $CADEF      : DEFINE ASSEMBLY SWITCHES
0000 137      $CLIDEF     : DEFINE CLI VALUES
0000 138      $IODEF      : DEFINE I/O FUNCTION CODES
0000 139      $IPLDEF     : DEFINE IPL VALUES
0000 140      $IRPDEF     : DEFINE IRP VALUES
0000 141      $PCBDEF     : DEFINE PROCESS CONTROL BLOCK
0000 142      $PRDEF      : DEFINE PROCESSOR REGISTERS
0000 143      $PRIDEF     : DEFINE PRIORITY INCREMENT CLASSES
0000 144      $PRTDEF     : DEFINE PROTECTION VALUES
0000 145      $PSLDEF     : DEFINE PSL FIELDS
0000 146      $SSDEF      : DEFINE SYSTEM SERVICE STATUS CODES
0000 147      $TRMDEF     : TERMINAL ITEMLIST DEFINITIONS
0000 148
0000 149      :
0000 150      : MACROS:
0000 151      :
0000 152
0000 153      :
0000 154      : EQUATED SYMBOLS:
0000 155      :
00000008 0000 156 V_F1=8      : FIELD 1 PRESENT FLAG
00000009 0000 157 V_F2=9      : FIELD 2 PRESENT FLAG
0000000A 0000 158 V_F3=10     : FIELD 3 PRESENT FLAG
0000000B 0000 159 V_F4=11     : FIELD 4 PRESENT FLAG
0000000C 0000 160 V_F5=12     : FIELD 5 PRESENT FLAG
0000000D 0000 161 V_INSTR=13   : INSTRUCTION DISPLAY MODE
0000      0000 162      : (OVERRIDES HEX OR ASCII & CURTYPE)
0000      0000 163
00000000 0000 164 V_OPEN=0     : OPEN CELL FLAG
00000001 0000 165 V_ASCII=1    : ASCII
00000002 0000 166 V_INFIELD=2  : FIELD IN PROGRESS
00000003 0000 167 V_TBIT=3     : ENABLE TBIT
00000004 0000 168 V_ATBRK=4    : AT BREAKPOINT
00000005 0000 169 V_TBITOK=5   : TBIT EXPECTED
00000006 0000 170 V_RUB=6      : RUBOUT IN PROGRESS
00000007 0000 171 V_NEGATE=7   : NEGATE BIT
0000000F 0000 172 V_PMODE=15   : PROCESSOR REGISTER MODE
0000001F 0000 173 V_PREG=31    : PROCESSOR REGISTER FLAG
0000      0000 174
00000000 0000 175 RDCR=0       : READ CSR
00000002 0000 176 RDBUF=2      : READ BUFFER
00000004 0000 177 OUTCR=4      : OUTPUT CSR
00000006 0000 178 OUTB=6       : OUTPUT BUFFER
0000      0000 179
0000005C 0000 180 BSLSH=92    : BACK SLASH CODE
0000000D 0000 181 CR=13       : CARRIAGE RETURN
0000000A 0000 182 LF=10       : LINE FEED
00000027 0000 183 QUOT=39     : QUOTE
0000007F 0000 184 RUBOUT=127  : RUBOUT CODE
0000002F 0000 185 SLSH=47     : SLASH CODE
0000      0000 186
```

```
0000 187
0000 188
0000 189
0000 190
0000 191
00000000 192
0000 193
0000 194
00000000 0000 195 DELBASE: .LONG
00001600 0004 196 .LONG
00000FC1 0008 197 .LONG
000C 198 .ENDC
000C 199
000C 200 CONTEXT:
00000000 000C 201 .LONG 0
00000060 0010 202 INBUF: .BLKB 80
00000000 0060 203 STATUS: .LONG 0
00000000 0064 204 F1: .LONG 0
00000000 0068 205 F2: .LONG 0
00000000 006C 206 F3: .LONG 0
00000000 0070 207 F4: .LONG 0
00000000 0074 208 F5: .LONG 0
0078 209
00000000 0078 210 MFYFLG: .LONG 0
007C 211
00000000 007C 212 PID: .LONG 0
00000000 0080 213 INSLEN: .LONG 0
00000000 0084 214 INSBUF: .LONG 0
0088 215
00 0088 216 FCTR: .BYTE 0
0089 217
02 0089 218 DTYPE: .BYTE 2
02 008A 219 CURTYPE: .BYTE 2
008B 220
00 008B 221 OPER: .BYTE 0
008C 222 B:
00000000 008C 223 CURDOT: .LONG 0
00000000 0090 224 QUAN: .LONG 0
000000A4 0094 225 OUTBUF: .BLKL 4
00A4 226
00A4 227
00A4 228
00A4 229 SAVREG:
000000A8 00A4 230 .BLKL 1
000000AC 00A8 231 .BLKL 1
000000B0 00AC 232 SAVR2: .BLKL 1
000000B4 00B0 233 .BLKL 1
000000B8 00B4 234 .BLKL 1
000000BC 00B8 235 .BLKL 1
000000C0 00BC 236 .BLKL 1
000000C4 00C0 237 .BLKL 1
000000C8 00C4 238 .BLKL 1
000000CC 00C8 239 .BLKL 1
000000D0 00CC 240 .BLKL 1
000000D4 00D0 241 .BLKL 1
000000D8 00D4 242 SAVAP: .BLKL 1
000000DC 00D8 243 .BLKL 1

OWN STORAGE:
.PSECT ZSDEBUG_CODE, LONG, PIC, EXE, WRT
.IF DF, SW_PROCESS
DELBASE-DELBASE : RELATIVE PAGE NUMBER OF WRITABLE
<511+DELEND-DELBASE>8^C511 : REL PAGE NUMBER OF END OF WRITABLE
DELTA_START-DELBASE : START ADDRESS
.ENDC

: BUFFER PADDING
: INPUT BUFFER
: STATUS FLAGS
: FIELDS
: 1-5

: MODIFY ENABLE FLAG FOR OTHER PROCESS
: ADDRESS SPACES
: PID FOR ADDRESS SPACE 0=>SELF
: LENGTH OF PREVIOUS INSTRUCTION
: ADDRESS OF INSTRUCTION STREAM BUFFER
: (FOR OUTPUT ADDRESS ROUTINE)
: FIELD COUNTER

: DATA TYPE
: CURRENT TYPE

: OPERATOR
: BASE OF DATA AREA(CENTER)
: CURRENT LOCATION
: QUANTITY (:Q)
: OUTPUT BUFFER

REGISTER SAVE AREA
: REGISTER SAVE AREA
: R0
: R1
: R2
: R3
: R4
: R5
: R6
: R7
: R8
: R9
: R10
: R11
: AP
: (FP)
```



```
000000E0 00DC 244 SAVSP: .BLKL 1      : SP
000000E4 00E0 245 SAVPC: .BLKL 1      : PC
000000E8 00E4 246 SAVPSL: .BLKL 1     : PSL
000000EA 00E8 247 SAVOCR: .BLKW 1     : OUTPUT CSR SAVE
000000EC 00EA 248 SAVRCR: .BLKW 1     : INPUT CSR SAVE
000000F0 00EC 249 ASTEN: 1           : AST ENABLE SAVE LOCATION
000000F0 00F0 250 SAVRXCS: .BLKL 1    : CONSOLE RECEIVER STATUS
000000E4 00F0 251 CONTEXTSZ=-CONTEXT : SIZE OF PER MODE CONTEXT AREA
000000E4 00F0 252 :
000000E4 00F0 253 : RESERVE SPACE FOR MULTIPLE MODE CONTEXT AREA
000000E4 00F0 254 :
000000E4 00F0 255 :
000000E4 00F0 256 .IF DF,SW_PROCESS :
000000E4 00F0 257 .REPT 3 :
000000E4 00F0 258 .BLKB CONTEXTSZ : FOR EXEC,SUPER AND USER
000000E4 00F0 259 SAV...= :
000000E4 00F0 260 .=-CONTEXTSZ+<DTYPE-CONTEXT> : POINT AT DTYPE,CURTYP
000000E4 00F0 261 .BYTE 2,2 : SET TYPE TO LONGWORD
000000E4 00F0 262 .=SAV... : RESTORE LOCATION COUNTER
000001D4 00F0 263 .ENDR :
000001D4 039C 264 .ENDC :
000001D4 039C 265 :
000001D4 039C 266 :
000001D4 039C 267 :
000001D4 039C 268 : BREAK POINT DATA
000001D4 039C 269 :
000001D4 039C 270 :
000001D4 00 039C 271 OVROPC: .BYTE 0 : OPCODE IN STEP-OVER BREAKPOINT
000001D4 039D 272 .ALIGN LONG :
000001D4 03A0 273 :
0000039C 03A0 274 BRKADR=-4 :
0000039C 03A0 275 .IF NDF,SW_PROCESS :
0000039C 03A0 276 XDELIBRK:: :
0000039C 03A0 277 .LONG INISBRK : ADDRESS OF INITIAL BREAKPOINT
0000039C 03A0 278 .IFF : FOR PROCESS VERSION
00000000 03A0 279 INIBRKA: .LONG 0 : INITIAL BREAKPOINT
00000000 03A4 280 .ENDC :
000003C0 03A4 281 .BLKL 7 : OTHER BREAK POINT ADDRESSES
00000008 03C0 282 NBRK=<.-4-BRKADR>/4 : NUMBER OF BREAKPOINTS
000003C4 03C0 283 OVRADR: .BLKL 1 : TEMPORARY BREAKPOINT FOR STEP-OVER
00000001 03C4 284 NTMPBRK=1 : NUMBER OF TEMPORARY BREAKPOINTS
000003C3 03C4 285 BRKOP=-1 : SAVED OPCODE
000003C3 03C4 286 NOP : INITIAL OPCODE
000003CC 03C5 287 .BLKB 7 : REMAINING OPCODES
000003CD 03CC 288 .BLKB 1 : TEMPORARY BREAKPOINT OPCODE
000003CD 03CD 289 :
000003C9 03CD 290 BRKDSP=-4 :
000003ED 03CD 291 .BLKL 8 : DISPLAY LOCATION START
000003E9 03ED 292 BRKCOM=-4 :
0000040D 03ED 293 .BLKL 8 : COMMAND START
0000040D 040D 294 :
00000419 040D 295 XREGV: .BLKL 3 : X REGISTER VECTOR
00000419 0419 296 .IF NDF,SW_PROCESS :
00000419 0419 297 XDEL_LOADBASE:: : BASE OF LOADABLE CPU DEPENDANT CODE
00000419 0419 298 .LONG 0 : X3 = BASE OF SYSLOA CODE
00000419 0419 299 .LONG SCH$GL_CURPCB : X4 = CURRENT PCB ADDRESS
00000419 0419 300 .LONG SCH$GL_PCBVEC : X5 = BASE OF PCB VECTOR
```

	0419	301	.LONG	PFNSAW_SWAPVBN	: X6 = SWAP VBN
	0419	302	.LONG	PFNSAL_PTE	: X7 = PTE BACK POINTER
	0419	303	.LONG	PFNSAL_BAK	: X8 = BACKUP ADDRESS
	0419	304	.LONG	PFNSAW_REFCNT	: X9 = REFERENCE COUNT
	0419	305	.LONG	PFNSAx_FLINK	: XA = FORWARD LINK
	0419	306	.LONG	PFNSAx_BLINK	: XB = BACK LINK
	0419	307	.LONG	PFNSAB_STATE	: XC = STATE
	0419	308	.LONG	PFNSAB_TYPE	: XD = TYPE
	0419	309		XDS\$GL_XESTRING::	
	0419	310	.LONG	XDS\$GT_WORD_PFN	: XE;E WITH X0 = PFN , DEFAULT TO WORD ARRAY
	0419	311		XDS\$GL_XFSTRING::	
	0419	312	.LONG	XDS\$GT_WORD_PFN	: XF;E WITH R0 = PFN , DEFAULT TO WORD ARRAY
	0419	313	.BLKL	1	: SAVED CONTENT OF MACHINE CHECK VECTOR
	0419	314	.IFF		: FOR PROCESS VERSION
0000044D	0419	315	.BLKL	13	
00000455	044D	316	.BLKL	2	: IO STATUS BLOCK FOR TERMINAL READ
00000459	0455	317	.BLKL	1	: CHANNEL NUMBER
00000002	0459	318	.LONG	2	: DESCRIPTOR OF INPUT/OUTPUT DEVICE
00000461	045D	319	.BLKL	1 ;TTNAMD+8	: (ADDRESS SET BY INITIALIZATION)
54 54	0461	320	.ASCII	/Tt/	
	0463	321		TTITMLST:	: THE ITEMLIST TO ALLOW DELTA TO TURN OFF ED
0000	0463	322	.WORD	0	
0000	0465	323	.WORD	TRMS_MODIFIERS	: SPECIFY THE MODIFIERS
00008000	0467	324	.LONG	TRMSM_TM_NOEDIT	: SPECIFY NO EDITING
00000000	046B	325	.LONG	0	
0010	046F	326	.WORD	TERMASKLEN	: LENGTH OF TERMINATOR MASK
0003	0471	327	.WORD	TRMS_TERM	: SPECIFY THE TERMINATOR MASK
	0473	328		TERMASKADR:	: ALLOW FOR RELOCATION
0000047B	0473	329	.BLKL	2	
00000018	047B	330		TTITMLSTLEN=-TTITMLST	
	047B	331		DBGINPUT:	
00000009	047B	332	.LONG	9	: DESCRIPTOR OF DEFAULT INPUT/OUTPUT
00000483	047F	333	.BLKL	1 ;DBGINPUT+8	
41 54 4C 45 44 24 47 42 44	0483	334	.ASCII	/DBG\$DELTA/	: FIRST DEFAULT DELTA INPUT
	048C	335		TRNINPUT:	
00000040	048C	336	.LONG	64	: TRANSLATED DBG\$DELTA
00000494	0490	337	.BLKL	1 ;TRNINPUT+8	: (ADDRESS SET BY INITIALIZATION)
000004D4	0494	338	.BLKB	64	
	04D4	339		DBGACTIVE:	: ACTIVE FLAGS BY ACCESS MODE
00000000	04D4	340	.LONG	0	
	04D8	341		EXITBLK:	: EXIT HANDLER BLOCK
00000000	04D8	342	.LONG	0	
000004E0	04DC	343	.BLKL	1 ;EXIHANDLE	: EXIT HANDLER (ADDRESS SET BY INIT)
00000001	04E0	344	.LONG	1	: ARGUMENT COUNT
000004E8	04E4	345	.BLKL	1 ;EXITCODE	: ADDRESS TO STORE STATUS (ADDRESS SET BY IN
	04E8	346		EXITCODE:	
00000001	04E8	347	.LONG	1	: RECEIVER FOR EXIT CODE
	04EC	348		KCOND_PRIMARY:	
00000000	04EC	349	.LONG	0	: PREVIOUS KERNEL PRIMARY HANDLER
	04F0	350		ECOND_PRIMARY:	
00000000	04F0	351	.LONG	0	: PREVIOUS EXEC PRIMARY HANDLER
	04F4	352		SCOND_PRIMARY:	
00000000	04F4	353	.LONG	0	: PREVIOUS SUPER PRIMARY HANDLER
	04F8	354		KCOND_LASTCHANC:	
00000000	04F8	355	.LONG	0	: PREVIOUS KERNEL LAST CHANCE HANDLER
	04FC	356		ECOND_LASTCHANC:	
00000000	04FC	357	.LONG	0	: PREVIOUS EXEC LAST CHANCE HANDLER

```
00000000 0500 358 SCOND_LASTCHANC:
00000000 0500 359 .LONG 0 ; PREVIOUS SUPER LAST CHANCE HANDLER
00000000 0504 360 TERMASK: ; TERMINATOR MASK DESCRIPTOR
08002600 0504 361 .LONG <129>!<1210>!<1213>!<1227> ; TAB,LF,CR,ESC
20008000 0508 362 .LONG <121>!<122>!<1215>!<1229> ; TAB,LF,CR,ESC
00088000 050C 363 .LONG <1215>!<1219> ; '0', 'S', '!', '!', '!', '!', '!', '!'
00000000 0510 364 .LONG 0 ;
00000010 0514 365 TERMASKLEN = .-TERMASK ;
0514 366 .ENDC ;
0514 367 ;
0514 368 ; LIST OF OPCODES WHICH CALL ROUTINES
0514 369 ;
0514 370 OVEROPCODES:
10 0514 371 .BYTE ^X10 ; BSBB
16 0515 372 .BYTE ^X16 ; JSB
30 0516 373 .BYTE ^X30 ; BSBW
FA 0517 374 .BYTE ^XFA ; CALLG
FB 0518 375 .BYTE ^XFB ; CALLS
00000005 0519 376 OVEROPCLEN = .-OVEROPCODES
```



```
42 41 39 38 37 36 35 34 33 32 31 30 0519 378 .SBTTL PRIMARY COMMAND CHARACTER SWITCH
46 45 44 43 0519 379
000000 12 0519 380 :
2E 0519 381 :
2C 0519 382 :
000000 2B 0519 383 PRIMARY:
20 0519 384 .ASCII /0123456789ABCDEF/ : DECIMAL AND HEX CHARS
40 0525 385 .ASCII /./ : DOT - CURRENT LOCATION
2A 0529 386 .ASCII /./ : COMMA - FIELD SEPARATOR
2D 052A 387 OPERBAS=-PRIMARY : OPERATORS
5B 052B 388 .ASCII /+/: PLUS - ADD
09 052C 389 .ASCII / /: BLANK - SAME AS PLUS
0A 052D 390 .ASCII /@/: SHIFT OPERATOR
0D 052E 391 .ASCII /*/: MULTIPLY OPERATOR
2F 052F 392 .ASCII /%/: DIVIDE OPERATOR
22 0530 393 .ASCII /-/: MINUS - SUBTRACT OPERATOR
3D 0531 394 .ASCII /[/: LBRACKET - LEFT BRACKET
1B 0532 395 TERM: : BASE OF TERMINATOR LIST
53 0533 396 .ASCII <9> : TAB - INDIRECT
4F 0534 397 .ASCII <10> : LINEFEED -
21 0535 398 .ASCII <CR> : RETURN -
0000000A 0536 400 .ASCII '': SLASH - OPEN FOR DISPLAY
3B 0537 401 .ASCII /=/: DOUBLE QUOTE - OPEN FOR ASCII DISPLAY
3A 0538 402 .ASCII <27> : EQUALS - DISPLAY
50 0539 403 .ASCII /S/: ESCAPE - PREVIOUS LOCATION
51 053A 404 .ASCII /O/: STEP
27 053B 405 .ASCII /!/: STEP-OVER ROUTINE
52 053C 406 NTERM=-TERM : DISPLAY INSTRUCTION
47 053D 407 .ASCII <59> : NUMBER OF TERMINATORS
48 053E 408 .ASCII /:/: SEMI - INITIATE SECONDARY
58 053F 409 .ASCII /P/: COLON - SEPARATE PID FORM ADDRESS
0000002C 0540 410 .ASCII /Q/: P - PROCESSOR REGISTER PREFIX
0541 411 .ASCII /'/: Q - LAST QUANTITY
0542 412 .ASCII /R/: QUOTE - BEGIN CHAR STRING
0543 413 .ASCII /G/: REGISTER PREFIX
0544 414 .ASCII /H/: G - GLOBAL PREFIX
0545 415 .ASCII /X/: H - HIGH, P1 SPACE PREFIX
NPRIM=-PRIMARY : X REGISTER PREFIX
: : NUMBER OF PRIMARY COMMANDS
```

```
00 0D 0A 3F 4B 45 0D 0A 0545 419 .SBTTL PRIMARY COMMAND SCANNER
                                0545 420
                                0545 421 :
                                0545 422 :
                                0545 423 :
                                0545 424 :
                                0545 425
                                0545 426 OUTER: .ASCIZ <LF><CR>/EH?/<LF><CR>
                                054D 427
                                054D 428 DCOM: .WORD
                                054F 429 .IF DF,SW PROCESS
                                054F 430 MOVAB W*DBGEXCEP,(FP)
                                0554 431 .ENDC
                                0554 432 BRB SCANP
                                0556 433 ERROR: MOVAB OUTER,R4
                                055A 434 BSBW OUTZSTRING
                                055D 435 SUPERST: MOVL FP,SP
                                0560 436 MOVAB INBUF-B(R11),R9
                                0564 437 CLRB (R9)
                                0566 438 BSBW RESET
                                0569 439 SCANP: BSBB NEXTP
                                056B 440 BRB SCANP
                                056D 441 NEXTP:
                                056D 442 BSBW GETCHAR
                                0570 443 LOCC RB,#NPRIM,PRIMARY
                                0575 444 BEOL ERROR
                                0577 445 SUBL3 RO,#NPRIM,RO
                                057B 446 CASE RO,LIMIT=#16,<-
                                057B 447 DOT,-
                                057B 448 COMMA,-
                                057B 449 OPERATOR,-
                                057B 450 OPERATOR,-
                                057B 451 OPERATOR,-
                                057B 452 OPERATOR,-
                                057B 453 OPERATOR,-
                                057B 454 NEGATE,-
                                057B 455 LBRACKET,-
                                057B 456 TAB,-
                                057B 457 LINEFEED,-
                                057B 458 RETURN,-
                                057B 459 SLASH,-
                                057B 460 DQUOTE,-
                                057B 461 EQUALS,-
                                057B 462 ESCAP,-
                                057B 463 STEP,-
                                057B 464 STEPOVER,-
                                057B 465 INSTR,-
                                057B 466 SEMI,-
                                057B 467 COLON,-
                                057B 468 PREG,-
                                057B 469 QUANT,-
                                057B 470 QUOTE,-
                                057B 471 REGISTER,-
                                057B 472 GLOBL,-
                                057B 473 HIGH,-
                                057B 474 XREG,-
                                057B 475 >

                                CALL ENTRY POINT
                                FOR PROCESS VERSION ONLY
                                SET CONDITION HANDLER ADDRESS

                                ENTER SCANP
                                SET ADDR OF CONTROL STRING
                                OUTPUT ASCIZ STRING
                                RESET STACK
                                RESET STRING ADDRESS
                                AND FORCE READ
                                RESET SCANNER
                                SCAN INPUT
                                SCAN IT ALL
                                PROCESS NEXT PRIMARY CHAR
                                GET CHARACTER
                                CHECK IT
                                NOT FOUND, ERROR
                                RATIONALIZE INDEX

                                DOT - CURRENT LOCATION
                                COMMA - FIELD SEPARATOR
                                PLUS - ADD OPERATOR
                                BLANK - ADD OPERATOR
                                @ - SHIFT OPERATOR
                                * - MULTIPLY OPERATOR
                                % - DIVIDE OPERATOR
                                MINUS - SUBTRACT/NEGATE
                                LEFT BRACKET - MODE SELECT
                                TAB - INDIRECT
                                LINE FEED - NEXT LOCATION
                                RETURN - CLOSE OPEN CELL
                                SLASH - OPEN FOR DISPLAY
                                DOUBLE QUOTE - OPEN FOR ASCII DISPLAY
                                EQUALS - DISPLAY VALUE
                                ESCAPE - PREVIOUS LOCATION
                                'S' - SINGLE STEP
                                'O' - STEP OVER ROUTINE CALL
                                '!' - DISPLAY INSTRUCTION
                                SEMI COLON - SECONDARY COMMAND
                                COLON - SEPARATE PID FROM ADDRESS
                                'P' - PROCESSOR REGISTER
                                'Q' - LAST QUANTITY
                                QUOTE - BEGIN ASCII STRING

                                G - GLOBAL PREFIX
                                H - P1 SPACE PREFIX
                                X REGISTER
```

DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
PRIMARY COMMAND SCANNER

N 14

15-SEP-1984 23:38:31 VAX/VMS Macro V04-00  
5-SEP-1984 00:08:35 [DELTA.SRC]XDELTA.MAR;1

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10	50	B1	05B7	476		CMPW	R0,#16	:	IS NUMBER > RADIX
	9A	18	05BA	477		BGEQ	ERROR	:	YES
56	10	C4	05BC	478		MULL	#16,R6	:	SCALE BY RADIX
56	50	C0	05BF	479		ADDL	R0,R6	:	AND ADD NEW DIGIT
6A	04	C8	05C2	480	INFLD:	BISL	#<12V_INFIELD>,(R10)	:	NOTE FIELD INPUT
		05	05C5	481		RSB		:	NEXT PRIMARY CHARACTER
			05C6	482					
			05C6	483					
54	01	1F	9C	05C6	484	GLOBL:	ROTL #31,#1,R4	:	GENERATE SYSTEM SPACE PREFIX
		07	11	05CA	485	BRB	PRE1	:	MERGE WITH COMMON
54	7FFE0000	8F	D0	05CC	486	HIGH:	#*X7FFE0000,R4	:	P1 SPACE BASE ADDRESS
		06	10	05D3	487	PRE1:	ENDEXPR	:	END EXPRESSION
	56	54	D0	05D5	488	MOVL	R4,R6	:	SET INTO ACCUM
	E7	AF	9F	05D8	489	PUSHAB	INFLD	:	RETURN THROUGH INFLD
				05DB	490	BRB	ENDEXPR		
				05DB	491				



```
.SBTTL ENDEXPR - END EXPRESSION

05DB 493
05DB 494
05DB 495
05DB 496
05DB 497
05DB 498 ENDEXPR:
05DB 499 BBCC #V_NEGATE,(R10),5$
05DF 500 R6-R6
05E2 501 5$: BSBB 10$
05E4 502 CLRL R6
05E6 503 CLRB OPER-B(R11)
05E9 504 RSB
05EA 505 10$: CASE OPER-B(R11),TYPE=B,<-
05EA 506 ADD,-
05EA 507 ADD,-
05EA 508 SHFT,-
05EA 509 MUL,-
05EA 510 DIV,-
05EA 511 >
57 57 56 78 05F9 512 SHFT: ASHL R6,R7,R7
05 05FD 513 RSB
57 56 C4 05FE 514 MUL: MULL R6,R7 ; MULTIPLY
05 0601 515 RSB ; AND EXIT
57 56 C6 0602 516 DIV: DIVL R6,R7 ; DIVIDE
05 0605 517 RSB ; AND EXIT
57 56 C0 0606 518 ADD: ADDL R6,R7 ; ADD
05 0609 519 RSB ; AND EXIT
060A 520
```

```
060A 522 .SBTTL SLASH - OPEN CELL
060A 523
060A 524 :
060A 525 :
060A 526 :
060A 527 :
060A 528 :
060A 529 :
060F 530 :
060F 531 :
060F 532 :
0614 533 :
0619 534 :
061B 535 :
061F 536 :
0623 537 :
0625 538 :
0629 539 :
062E 540 :
0633 541 :
0636 542 :
063A 543 :
063E 544 :
0640 545 :
0643 546 :
0645 547 :
0648 548 :

        6A 02 88 060A 522
        05 11 060A 523
        06 2002 8F AA 060A 524
        6A 2000 8F AA 060A 525
        4E 10 060A 526
        06 6A 08 E0 060A 527
        6B 04 AB D0 060A 528
        04 11 060A 529
        6B 08 AB D0 060F 530
        50 6A 01 0F EF 060F 531
        6A 01 1F 50 F0 060F 532
        00B7 30 060F 533
        22 6A 09 E1 0614 534
        6B DC AB D1 0619 535
        1C 15 061B 536
        00A5 30 061F 537
        F5 11 0623 538
        FFOE 31 0625 539
        0629 540
        062E 541
        0633 542
        0636 543
        063A 544
        063E 545
        0640 546
        0643 547
        0645 548

        SLASH:
        OPEN:
        5$:
        10$:
        15$:
        ERR4:

        BICW #<12V_ASCII>,(R10)
        BRB OPEN
        BICW #<12V_ASCII>!<12V_INSTR>,(R10)
        BICW #<12V_INSTR>,(R10)
        BSBB ENDFIELD
        BBS #V F1,(R10),5$
        MOVL QUAN-B(R11),CURDOT-B(R11)
        BRB 10$
        MOVL F1-B(R11),CURDOT-B(R11)
        EXTZV #V_PMODE,#1,(R10),R0
        INSV R0,#V_PREG,#1,(R10)
        BSBW LOCOUT
        BBC #V F2,(R10),RSET
        CMPL F2-B(R11),CURDOT-B(R11)
        BLEQ RSET
        BCBW NEXTLOC
        BRB 15$
        BRW ERROR

        : DISPLAY IN ASCII
        : SET ASCII FLAG
        :
        : (R10) : CLEAR ASCII DISPLAY MODE
        : CLEAR INSTRUCTION FLAG
        : TERMINATE FIELD
        : ADDR SPECIFIED?
        : NO, GO INDIRECT
        : AND DISPLAY CONTENT
        : SET NEW DOT
        : GET PROCESSOR REGISTER MODE FLAG
        : AND MOVE TO SEMI-PERMANENT COPY
        : OUTPUT AND OPEN
        : RANGE SPECIFIED?
        : CHECK FOR END
        : YES
        : INCREMENT TO NEXT DOT
        : AND CONTINUE
        : DECLARE ERROR
```

			0648	550	.SBTTL	RETURN - CLOSE CURRENT OPEN CELL	
			0648	551			
			0648	552			
			0648	553			
			0648	554			
			0648	555			
			0648	556	RETURN:		
	1F	10	0648	557	BSBB	ENDFIELD	TERMINATE CURRENT FIELD
			064A	558	.ENABL	LSB	
11 6A	00	E5	064A	559	BBCC	#V OPEN, (R10), 108	SKIP IF NONE OPEN
6A	2002	8F	064E	560	BITW	#<T2V_ASCII>!<T2V_INSTR>, (R10)	; IF ASCII OR INSTRUCTION
	07	12	0653	561	BNEQ	RSET	DISPLAY MODE SKIP STORE OPERATION
03 6A	08	E1	0653	562	BBC	#V F1, (R10), RSET	SKIP IF NOTHING TO STORE
	0887	30	0659	563	BSBW	DEPOSIT	DEPOSIT
	046D	31	065C	564	RSET:	RSET	RESET SCANNER
F9 6A	08	E1	065F	565	108:	BBCC	#V F1, (R10), RSET
	045E	31	0663	566	BRW	EQC1	DONE IF NO INPUT
			0666	567	.DSABL	LSB	OTHERWISE OUTPUT



```
0666 569 .SBTTL ENDFIELD - TERMINATE CURRENT FIELD
0666 570
0666 571 ::
0666 572 :: COMMA TERMINATE CURRENT FIELD
0666 573 ::
FF59 30 0666 574 COMMA: BSBW INFLD : ZERO IF NULL FIELD
0669 575
0669 576 ::
0669 577 :: TERMINATE CURRENT FIELD
0669 578 ::
0669 579 ENDFIELD:
16 6A 02 E5 0669 580 BBCC #V INFIELD,(R10),10$ : CLEAR PENDING FIELD
FF6B 30 066D 581 BSBW ENDEXPR : END EXPRESSION
50 FC AB 9A 0670 582 MOVZBL FCTR-B(R11),R0 : GET FIELD POINTER
CC 01 AA 50 E2 0674 583 BBSS R0,1(R10),ERR4 : ERROR IF TOO MANY FIELDS
DB AB40 57 D0 0679 584 MOVL R7,F1-B(R11)[R0] : STORE FIELD VALUE
FC AB 96 067E 585 INCB FCTR-B(R11) : INCREMENT FIELD COUNTER
56 7C 0681 586 CLRQ R6 : CLEAR ACCUMULATORS
05 0683 587 10$: RSB : RETURN
0684 588
```

```
0684 590 .SBTTL  FETCH - OBTAIN DATA SPECIFIED
0684 591
0684 592
0684 593
0684 594
0684 595
0684 596
0688 597
0688 598
068B 599
068D 600
068D 601
068D 602
068D 603
068D 604
068D 605
04 AB 00 BB 9A 0698 606 10$: MOVZBL @CURDOT-B(R11),QUAN-B(R11) ; GET BYTE
05 069D 607 RSB RETURN ;
04 AB 00 BB 3C 069E 608 20$: MOVZWL @CURDOT-B(R11),QUAN-B(R11) ; GET WORD
05 06A3 609 RSB RETURN ;
04 AB 00 BB D0 06A4 610 30$: MOVL @CURDOT-B(R11),QUAN-B(R11) ; GET LONGWORD
05 06A9 611 RSB RETURN ;
06AA 612 .IF NDF,SW_PROCESS
06AA 613 40$: MFPR CURDOT-B(R11),QUAN-B(R11) ; GET PROCESSOR REGISTER
06AA 614 RSB
06AA 615 .IFF FALSE IF PROCESS VERSION
06AA 616 40$: $CHKRNL_S B^FTCHPREG,(AP) ; CALL IN KERNEL MODE TO FETCH
05 06AA 617 RSB
0D41 31 06B6 618 50$: BRW FETCHP ; FETCH FROM FOREIGN PROCESS
06B7 619 .ENDC
06BA 620
06BA 621
06BA 622 .IF DF,SW_PROCESS
06BA 623 FTCHPREG:
06BA 624 .WORD 0 ENTRY MASK
068C 625 MOVAB W^PREXC,(FP) SET EXCEPTION HANDLER
06C1 626 MFPR CURDOT-B(R11),QUAN-B(R11) ; GET PROCESSOR REGISTER
06C5 627 MOVL #1,R0 RETURN SUCCESS
06C8 628 RET
06C9 629
06C9 630 .ENDC
```

```
06C9 632 .SBTTL NEXTDOT - INCREMENT CURRENT LOCATION
06C9 633
06C9 634 :
06C9 635 : INCREMENT TO NEXT LOCATION
06C9 636 :
06C9 637 NEXTDOT:
06C9 638 BBS #V_INSTR,(R10),20$ : BRANCH IF INSTRUCTION MODE
06CD 639 MOVL #1,R1 : ASSUME UNIT INCREMENT
06D0 640 TSTL (R10) : CHECK FOR PREG
06D2 641 BLSS 10$ : YES, USE UNIT INCREMENT
06D4 642 ROTL CURTYPE-B(R11),R1,R1 : FORM INCREMENT
06D9 643 10$: ADDL R1,CURDOT-B(R11) : AND ADD TO DOT
06DC 644 RSB : RETURN
06DD 645 20$: ADDL INSLEN-B(R11),CURDOT-B(R11) ; SKIP OVER PREVIOUS INSTRUCTION
06E1 646 RSB
06E2 647
```



PC	OC	04	06E2	649	.SBTTL	OUTPUT - DISPLAY CONTENT	:
			06E2	650	:		
			06E2	651	:	OUTPUT CONTENT	
			06E2	652	:		
			06E2	653	OUTBB:		
			06E2	654	.BYTE	4,12,28	; STARTING DIGIT LIST
			06E3	655			
			06E3	656	.SBTTL	LINE FEED - DISPLAY NEXT	
			06E3	657			
			06E3	658	LINEFEED:		:
	FF60	30	06E3	659	BSBW	RETURN	; CLOSE OPEN CELL
			06E8	660	NEXTLOC:		; PROMPT WITH NEXT LOCATION
	DF	10	06E8	661	BSBB	NEXTDOT	; INCREMENT LOCATION
			06EA	662	LOCPROMPT:		; DISPLAY ADDR/CONTENT
	0161	30	06EA	663	BSBW	OUTPUTA	; OUTPUT ADDRESS
31	6A	0D	06ED	664	LOCOUT:	BBS	#V INSTR,(R10),OUTINS
			06F1	665	BSBB	FETCH	; BRANCH IF INSTRUCTION MODE
	6A	01	06F3	666	BISB	#<10V_OPEN>,(R10)	; FETCH CONTENT
			06F6	667			; INDICATE OPEN CELL
			06F6	668	OUTPUT:		:
51	FE	AB	06F6	669	MOVZBL	CURTYPE-B(R11),R1	; GET TYPE
52	E4	AF41	06FA	670	MOVZBL	OUTBB[R1],R2	; INIT DIGIT SELECTOR
53	04	AB	06FF	671	MOVL	QUAN-B(R11),R3	; GET QUANTITY TO DISPLAY
05	6A	01	0703	672	BBS	#V ASCII,(R10),10\$	; CHECK FOR ASCII OUT
			0707	673	BSBW	OUTCOM	; OUTPUT NUMBER IN HEX
			070A	674	BRB	20\$	; AND EXIT THROUGH OUTSPACE
08	AB	53	070C	675	10\$:	MOVL	R3,OUTBUF-B(R11)
52	01	51	0710	676		ASHL	R1,#1,R2
	08	AB42	0714	677		CLRB	OUTBUF-B(R11)[R2]
			0718	678		BSBW	OUTZBUF
			071B	679	20\$:	BRW	OUTSPACE
			071E	680			; FOLLOW WITH SPACE

```
00 09 20 20
50 00000000'GF 9E 071E 682 .SBT1 OUTINS - OUTPUT INSTRUCTION
    07 12 071E 683 :
6A 2000 8F AA 071E 684 : OUTPUT RANGE OF INSTRUCTIONS
    BB 11 071E 685 :
SE 00000052 8F C2 071E 686 SPACES: .ASCIZ ' ' : 2 SPACES AND A TAB
    F8 AB 5E D0 0722 687 :
    54 08 D0 0722 688 :
    55 5E D0 0722 689 : .WEAK LIBSINS_DECODE : INSTRUCTION DECODE IS OPTIONAL
    FE AB 02 90 0722 690 OUTINS: MOVAB G^LIBSINS_DECODE,R0 : GET ADDRESS OF INSTRUCTION DECODER
    FF 38 30 0722 691 BNEQ 5$ : BRANCH IF LINKED WITH DECODER
    85 04 AB D0 0729 692 BICW #12V INSTR,(R10) : SUPPRESS INSTRUCTION MODE
    6B 04 C0 072B 693 BRB LOCOOT : AND PRINT 1ST LONGWORD OF INS STREAM
    F3 54 F5 0730 694 5$: SUBL #32+50,SP : ALLOCATE SPACE FOR INSTRUCTION STREAM
    6B 8E D0 0732 695 : AND DECODE OUTPUT BUFFER
    55 DD 0739 696 MOVL SP,INSBUF-B(R11) : SAVE ADDRESS FOR OUTPUT_ADDRESS
    32 DD 0739 697 MOVL #32/4,R4 : SET ITERATION COUNT
    9D AF 9F 0740 698 MOVL SP,R5 : SET POINTER INTO BUFFER
    04 AE 3F 0743 699 MOVAB #2,CURTYPE-B(R11) : SET FOR LONGWORD FETCHES
    08 AE 7F 0747 700 PUSHL CURDOT-B(R11) : SAVE CURRENT LOCATION COUNTER
    F8 AB DF 0749 701 10$: BSBW FETCH : FETCH LONGWORD
    00000000'GF 04 FB 074C 702 MOVL QUAN-B(R11),(R5)+ : STORE INTO INSTRUCTION BUFFER
    53 8E 7D 0750 703 ADDL #4,CURDOT-B(R11) : SKIP TO NEXT LONGWORD
    1A 50 E9 0753 704 SOBGR R4,10$ : FILL ENTIRE BUFFER
    64 43 94 0756 705 POPL CURDOT-B(R11) : RESTORE CURRENT LOCATION
    01 69 30 0759 706 PUSHL R5 : ADDRESS OF DECODE OUTPUT BUFFER
    F4 AB F8 AB 5E C3 075B 707 PUSHL #50 : LENGTH OF DECODE OUTPUT BUFFER
    SE 00000052 8F C0 075D 708 PUSHAB B^OUTPUT_ADDRESS : ADDRESS OF SYMBOLIZE ROUTINE
    54 92 AF 9E 0760 709 PUSHAW 4(SP) : ADDRESS OF WORD TO RECEIVE LENGTH
    01 55 31 0763 710 PUSHAB 8(SP) : ADDRESS OF DECODE OUTPUT DESCRIPTOR
    F8 AB DF 0766 711 PUSHAL INSBUF-B(R11) : ADDRESS OF INSTRUCTION STREAM POINTER
    53 8E 7D 0769 712 CALLS #4,G^LIBSINS_DECODE : DECODE INSTRUCTION INTO BUFFER
    1A 50 E9 0770 713 MOVQ (SP)+,R3 : GET DESCRIPTOR OF STRING
    64 43 94 0773 714 BLBC R0,90$ : BRANCH IF ERROR DETECTED
    01 69 30 0776 715 CLRB (R4)[R3] : MAKE INTO ASCIZ STRING
    F4 AB F8 AB 5E C3 0779 716 BSBW OUTZSTRING : OUTPUT ASCIZ STRING
    SE 00000052 8F C0 077C 717 SUBL3 SP,INSBUF-B(R11),INLEN-B(R11) : SET LENGTH OF INSTRUCTION
    54 92 AF 9E 0782 718 50$: ADDL #32+50,SP : DEALLOCATE STREAM/DECODE BUFFERS
    01 55 31 0789 719 MOVAB SPACES,R4 : SET ADDRESS OF SPACES
    0790 720 BRW OUTZSTRING : FOLLOW INSTRUCTION WITH SOME SPACE
    0790 721 :
    0790 722 : UNABLE TO DECODE INSTRUCTION (ACCVIO OR NEW INSTRUCTION). OUTPUT LONGWORD
    53 F8 BB D0 0790 723 :
    F4 AB 01 D0 0790 724 90$: MOVL @INSBUF-B(R11),R3 : GET FIRST LONGWORD OF STREAM
    01 2D 30 0794 725 MOVL #1,INLEN-B(R11) : SET INSTRUCTION LENGTH TO 1
    E5 11 0798 726 BSBW OUTLONG : OUTPUT AS LONGWORD
    0798 727 BRB 50$ :
    079D 728 :
    079D 729 : OUTPUT AN OPERAND WHICH IS A RELATIVE OR ABSOLUTE ADDRESS
    079D 730 :
    079D 731 :
    079D 732 :
    079D 733 OUTPUT_ADDRESS:
    081C 079D 734 .WORD ^M<R2,R3,R4,R11>
    53 04 BC D0 079F 735 :
    52 08 AC D0 07A3 736 MOVL @4(AP),R3 : GET VALUE (ARGUMENT BY REFERENCE)
    19 10 BC E8 07A7 737 MOVL 8(AP),R2 : GET ADDRESS OF DESCRIPTOR
    07A7 738 BLBS @16(AP),5$ : BRANCH IF ABSOLUTE ADDRESS
```

51	51	02	51	DC	07AB	739	.IF	DF, SW_PROCESS	: IF PROCESS VERSION,	
	51	18	18	EF	07AB	740	MOVPSL	R1	: GET CURRENT PSL	
	51	00E4	8F	A4	07AD	741	EXTZV	#PSLSV_CURMOD, #PSLSS_CURMOD, R1, R1	: ISOLATE CURRENT ACCESS MODE	
5B		FBD0	CF41	9E	07B2	742	MULW	#CONTEXT\$2, R1	: COMPUTE OFFSET FROM KERNEL CONTEXT	
					07B7	743	MOVAB	W*BLR1], R11	: GET BASE ADDRESS OF CONTEXT AREA	
					07BD	744	.IFF		: FOR EXECUTIVE VERSION,	
					07BD	745	MOVAB	W*B, R11	: GET BASE ADDRESS OF CONTEXT AREA	
					07BD	746	.ENDC			
53	F8	AB		C2	07BD	747	SUBL	INSBUF-B(R11), R3	: GET OFFSET FROM INSTRUCTION	
53	68			C0	07C1	748	ADDL	CURDOT-B(R11), R3	: AND COMPUTE "REAL" ADDRESS	
04	AB	53		D0	07C4	749	5B:	MOVL	R3, QUAN-B(R11)	: SET NEW "Q" SO THAT INDIRECTION
					07C8	750				: CAN BE USED TO SEE THE LAST OPERAND
					07C8	751				: OR BRANCHED-TO INSTRUCTION
	0C	BC		B4	07C8	752	CLRW	@12(AP)		: ASSUME NOTHING PUT INTO BUFFER
08	62			B1	07CB	753	CMPL	(R2), #8		: ENOUGH ROOM FOR 8 CHARACTERS?
	39			19	07CE	754	BLSS	20\$		: BRANCH IF NOT ENOUGH
	04	A2		D0	07D0	755	PUSHL	4(R2)		: SAVE ADDRESS OF RESULT BUFFER
51	53			D0	07D3	756	MOVL	R3, R1		: COPY EFFECTIVE ADDRESS
	0045			30	07D6	757	BSBW	RELOC		: SEE IF CLOSE TO RELOCATION REGISTER
	1F			19	07D9	758	BLSS	8\$		: BRANCH IF NONE FOUND
52	54			D0	07DB	759	MOVL	R4, R2		: SAVE OFFSET FROM X REGISTER
	54	8ED0			07DE	760	POPL	R4		: GET ADDRESS OF OUTPUT BUFFER
84	5B	8F		90	07E1	761	MOVB	#*A'X', (R4)+		: WRITE 'X'
		50		D4	07E5	762	CLRL	R0		: PRINT ONLY 1 DIGIT
		24		10	07E7	763	BSBB	100\$		: WRITE REGISTER NUMBER
84	2B			90	07E9	764	MOVB	#*A'+', (R4)+		: WRITE '+'
50	08			D0	07EC	765	MOVL	#8, R0		: PRINT 3 DIGITS
53	52			D0	07EF	766	MOVL	R2, R3		: RETRIEVE OFFSET FROM X REGISTER
	19			10	07F2	767	BSBB	100\$		: WRITE OFFSET IN HEX
0C	BC	06		B0	07F4	768	MOVW	#6, @12(AP)		: STORE LENGTH OF STRING
	0F			11	07F8	769	BRB	20\$		: EXIT SUCCESSFULLY
53	51			D0	07FA	770	8\$:	MOVL	R1, R3	: GET EFFECTIVE ADDRESS
	54	8ED0			07FD	771	POPL	R4		: GET ADDRESS OF OUTPUT BUFFER
50	1C			D0	0800	772	MOVL	#28, R0		: SET STARTING BIT FOR 1ST DIGIT
	08			10	0803	773	BSBB	100\$		: OUTPUT HEX LONGWORD
0C	BC	08		B0	0805	774	MOVW	#8, @12(AP)		: RETURN LENGTH TO CALLER
	50	01		D0	0809	775	20\$:	MOVL	#1, R0	: SUCCESS
				04	080C	776	RET			
					080D	777				
51	53	04	50	EF	080D	778	100\$:	EXTZV	R0, #4, R3, R1	: GET DIGIT
84	FD02	CF41		90	0812	779	MOVB	PRIMARY[R1], (R4)+		: MOVE DIGIT INTO BUFFER
	50	04		C2	0818	780	SUBL	#4, R0		: SKIP TO NEXT DIGIT
		F0		18	081B	781	BGEQ	100\$		: LOOP UNTIL END OF LONGWORD
				05	081D	782	RSB			

```
081E 784 .SBTTL DETERMINE CLOSEST RELOCATION REGISTER
081E 785 :
081E 786 : RELOC - GIVEN AN ADDRESS, RETURN CLOSEST RELOCATION REGISTER, IF ANY.
081E 787 :
081E 788 : INPUTS:
081E 789 :
081E 790 : R1 = ADDRESS
081E 791 :
081E 792 : OUTPUTS:
081E 793 :
081E 794 : X1 = EFFECTIVE ADDRESS
081E 795 : R3 = REGISTER #
081E 796 : R4 = OFFSET FROM X REGISTER
081E 797 : PSL CONDITION CODES SET ON R3
081E 798 : R2 DESTROYED.
081E 799 :
081E 800 RELOC: CLRL R3 : START WITH X0
081E 801 MNEGL #1,R2 : X REGISTER CLOSEST TO ADDRESS
081E 802 MOVZWL R2,R4 : CLOSEST SO FAR IS FFFF
081E 803 10$: MOVL XREGV[R3],R0 : GET X REGISTER
081E 804 BEQL 15$ : BRANCH IF NOT VALID
081E 805 SUBL3 R0,R1,R0 : GET OFFSET FROM X#
081E 806 CMPL R0,#^X800 : WITHIN REASONABLE RANGE?
081E 807 BGEQU 15$ : BRANCH IF OK
081E 808 CMPL R0,R4 : CLOSER THAN CLOSEST SO FAR?
081E 809 BGTRU 15$ : BRANCH IF NOT
081E 810 MOVL R3,R2 : SAVE X# CLOSEST TO ADDRESS
081E 811 MOVL R0,R4 : AND SET NEW CLOSEST OFFSET
081E 812 15$: AOBLSS #8,R3,10$ : LOOP UNTIL LAST REGISTER TESTED
081E 813 MOVL R2,R3 : RETURN X# CLOSEST TO ADDRESS
081E 814 RSB
```

50	FBE2	CF43	D4	081E	800
50	51	50	CE	0820	801
00000800	8F	50	3C	0823	802
		18	DO	0826	803
		50	13	082C	804
	54	50	C3	082E	805
		08	D1	0832	806
		50	1E	0839	807
		06	D1	083B	808
	52	53	1A	083E	809
	54	50	DO	0840	810
DC	53	08	DO	0843	811
	53	52	F2	0846	812
			DO	084A	813
			05	084D	814



				816	.SBTTL	OUTPUTA - OUTPUT ADDRESS	
				817	:	OUTPUT ADDRESS	
				818	:		
				819	:		
				820	OUTPUTA:	OUTPUT ADDRESS	
	0140	30	084E	821	BSBW	CRLF	OUTPUT CR/LF
51	68	D0	0851	822	MOVL	CURDOT-B(R11),R1	GET ADDRESS
	C8	10	0854	823	BSBB	RELOC	SEE IF CLOSE TO RELOCATION REGISTER
	1B	19	0856	824	BLSS	28	BRANCH IF NOT
	54	D0	0858	825	PUSHL	R4	SAVE OFFSET FROM RELOCATION REGISTER
50	58 8F	9A	085A	826	MOVZBL	#A'X',R0	OUTPUT AN 'X'
	00E1	30	085E	827	BSBW	OUTCHAR	
	52	D4	0861	828	CLRL	R2	PRINT ONLY 1 HEX DIGIT
	66	10	0863	829	BSBB	OUTCOM	OUTPUT HEX VALUE IN R3
50	2B	9A	0865	830	MOVZBL	#A'+',R0	OUTPUT AN '+'
	00D7	30	0868	831	BSBW	OUTCHAR	
52	08	D0	086B	832	MOVL	#8,R2	
	53 8ED0	086E	833	POPL	R3		PRINT 3 DIGITS
	49	11	0871	834	BRB	108	GET OFFSET FROM RELOCATION REGISTER
53	18 AB	9E	0873	835	MOVAB	SAVREG-B(R11),R3	OUTPUT OFFSET AND SLASH
	F0 AB	D5	0877	836	.IF	DF,SW_PROCESS	BASE OF REGISTER AREA
	19	12	0877	837	TSTL	PID-B(R11)	ONLY FOR PROCESS VERSION
			087A	838	BNEQ	38	CHECK FOR OTHER PROCESS ADDRESS
			087C	839	.ENDC		OR IF YES
53	68	53	087C	840	SUBL3	R3,CURDOT-B(R11),R3	COMPUTE OFFSET INTO REGISTER AREA
	26	19	0880	841	BLSS	58	NOT GENERAL REGISTER
53	04	C6	0882	842	DIVL	#4,R3	SCALE TO LONGWORD NUMBER
OF	53	D1	0885	843	CMPL	R3,#15	CHECK FOR MAX REG NUMBER
	1E	14	0888	844	BGTR	58	GTR, NOT A REGISTER
50	52 8F	9A	088A	845	MOVZBL	#A'R',R0	OUTPUT PREFIX
	00B1	30	088E	846	BSBW	OUTCHAR	OF 'R'
	52	D4	0891	847	CLRL	R2	AND SET FOR ONE DIGIT OF OUTPUT
	27	11	0893	848	BRB	108	
			0895	849	.IF	DF,SW_PROCESS	FOR PROCESS VERSION ONLY
	6A	D5	0895	850	TSTL	(R10)	CHECK FOR PROCESSOR REGISTER
	OF	19	0897	851	BLSS	58	OR IF YES
52	1C	D0	0899	852	MOVL	#28,R2	SET FOR LONGWORD OUTPUT
53	F0 AB	D0	089C	853	MOVL	PID-B(R11),R3	GET PID OF TARGET
	29	10	08A0	854	BSBB	OUTCOM	OUTPUT PID AS LONGWORD
50	5A	9A	08A2	855	MOVZBL	#A':',R0	SEPARATE WITH ':'
	009A	30	08A5	856	BSBW	OUTCHAR	OUTPUT COLON
			08A8	857	.ENDC		
53	68	D0	08A8	858	MOVL	CURDOT-B(R11),R3	GET ADDRESS
52	1C	D0	08AB	859	MOVL	#28,R2	ASSUME LONGWORD OUTPUT
	6A	D5	08AE	860	TSTL	(R10)	CHECK FOR PROCESSOR REGISTER
	0A	18	08B0	861	BGEQ	108	NO, JUST A LONGWORD
50	50 8F	9A	08B2	862	MOVZBL	#A'P',R0	PRECEDE WITH A 'P'
	0089	30	08B6	863	BSBW	OUTCHAR	OUTPUT P
52	04	D0	08B9	864	MOVL	#4,R2	SET FIELD TO 2 DIGITS
	0D	10	08BC	865	BSBB	OUTCOM	COMMON OUTPUT
50	2F	9A	08BE	866	MOVZBL	#SLSH,R0	OUTPUT SLASH
	007E	31	08C1	867	BRW	OUTCHAR	RETURN THROUGH OUTCHAR
			08C4	868	OUTDIGIT:		OUTPUT ONE DIGIT
	52	D4	08C4	869	CLRL	R2	ZAP DIGIT SELECTOR
	03	11	08C6	870	BRB	OUTCOM	AND MERGE WITH COMMON
			08C8	871			
			08C8	872	OUTLONG:		OUTPUT LONGWORD

52	1C	DO	08CB	873	MOVL	#28,R2	: SET DIGIT SELECTOR	
			08CB	874	OUTCOM:		: FORMAT IT	
51	54	08 AB	9E	08CB	875	MOVAB	OUTBUF-B(R11),R4	
	53	04	52	EF	876	EXTZV	R2,#4,R3,R1	
84	FC40	CF41	90	08D4	877	MOVAB	PRIMARY[R1],(R4)+	
	52	04	C2	08DA	878	SUBL	#4,R2	
		F0	18	08DD	879	BGEQ	10\$	
	54	08 AB	9E	08DF	880	CLRB	(R4)	
				08E1	881	OUTZBUF:MOVAB	OUTBUF-B(R11),R4	
				08E5	882		: GET START OF BUFFER	
				08E5	883	OUTZSTRING:	: OUTPUT ASCIZ STRING	
				08E5	884	.IF	NDF,SW_PROCESS	
				08E5	885	MOVZBL	(R4)+,R0	
				08E5	886	BEQL	10\$	
				08E5	887	BSBB	OUTCHAR	
				08E5	888	BRB	OUTZSTRING	
				08E5	889	10\$:	RSB	
				08E5	890	.IFF		
64	0100	8F	55	DD	08E5	891	PUSHL	R5
	55	51	00	3A	08E7	892	LOCC	#0,#256,(R4)
			54	C3	08ED	893	SUBL3	R4,R1,R5
			42	13	08F1	894	BEQL	90\$
					08F3	895	50\$:	SOIOW,S
					08F3	896		EFN=#30,-
					08F3	897		CHAN=TTCHAN,-
					08F3	898		FUNC=#10\$WRITEVBLK,-
					08F3	899		P1=(R4),-
					08F3	899		P2=R5
0124	8F	50	B1	0910	900	CMPW	R0,SS\$INSFMEM	
		13	B1	0915	901	BEQL	60\$	
	1C	50	B1	0917	902	CMPW	R0,SS\$EXQUOTA	
		0E	13	091A	903	BEQL	60\$	
2A00	8F	50	B1	091C	904	CMPW	R0,SS\$EXQUOTASTRT	
		12	1F	0921	905	BLSSU	90\$	
2AFF	8F	50	B1	0923	906	CMPW	R0,SS\$EXQUOTAEND	
		0B	1A	0928	907	BGTRU	90\$	
				092A	908	60\$:	SWAITFR,S	
		BE	11	0933	909	BRB	EFN=#30	
		55	8ED0	0935	910	90\$:	POPL	
			05	0938	911	RSB	R5	
				0939	912	.ENDC	: Restore saved register.	
				0939	913			
				0939	914			
				0939	915	OUTBSLSH:	: OUTPUT BACK SLASH	
50	5C	8F	9A	0939	916	MOVZBL	#BSLSH,R0	
		03	11	093D	917	BRB	OUTCHAR	
	50	58	9A	093F	918	OUTR8: MOVZBL	R8,R0	
				0942	919	OUTCHAR:		
				0942	920	.IF	NDF,SW_PROCESS	
				0942	921	TSTL	AP	
				0942	922	BNEQ	10\$	
				0942	923	JMP	G*CON\$PUTCHAR	
				0942	924		: CHECK FOR CONSOLE	
				0942	925	10\$:	NO,USE_DEVICE_DIRECTLY	
				0942	926	MOVW	OUTCR(AP),R1	
				0942	927	BBC	#7,R1,10\$	
				0942	928	MOVAB	R0,OUTB(AP)	
				0942	929	.IFF	: OUTPUT CHAR	
				0942	929	PUSHL	R0	
							: FALSE FOR PROCESS VERSION	
							: BUFFER CHARACTER ON STACK	

50	SE	D0	0944	930	50\$:	MOVL	SP,RO	:	SAVE POINTER TO IT
			0947	931		\$QIOW,S	EFN=#30,-	:	
			0947	932			CHAN=TTCHAN,-	:	
			0947	933			FUNC=#10\$WRITEVBLK,-	:	
			0947	934			P1=(RO),-	:	
			0947	935			P2=#1	:	
0124	8F	50	0964	936		CMPW	RO,#SS\$_INSFMEM	:	BUFFER ADDRESS
		13	0969	937		BEQL	60\$	:	ONE CHARACTER
1C		50	096B	938		CMPW	RO,#SS\$_EXQUOTA	:	If any resource error occurs,
		0E	096E	939		BEQL	60\$	:	wait for an I/O completion
2A00	8F	50	0970	940		CMPW	RO,#SS\$_EXQUOTASTRT	:	and try again.
		12	0975	941		BLSSU	90\$	:	
2AFF	8F	50	0977	942		CMPW	RO,#SS\$_EXQUOTAEND	:	
		0B	097C	943		BGTRU	90\$	:	
			097E	944	60\$:	\$WAITFR,S	EFN=#30	:	
		BB	0987	945		BRB	50\$	:	
		01	0989	946	90\$:	POPR	#*M<RO>	:	RESTORE CHARACTER
			098B	947		.ENDC		:	
		05	098B	948		RSB		:	AND RETURN
			098C	949	OUTSPACE:			:	
50	20	9A	098C	950		MOVZBL	#32,RO	:	SET CODE FOR SPACE
		B1	098F	951		BRB	OUTCHAR	:	AND SEND IT
50	0D	9A	0991	952	CRLF:	MOVZBL	#CR,RO	:	RETURN
		AC	0994	953		BSBB	OUTCHAR	:	SEND IT
50	0A	9A	0996	954		MOVZBL	#LF,RO	:	LINE FEED
		A7	0999	955		BRB	OUTCHAR	:	SEND IT
			099B	956				:	
			099B	957				:	

```
099B 959 .SBTTL GETCHAR - GET INPUT CHARACTER ROUTINE
099B 960
099B 961 :
099B 962 GETCHAR - GET INPUT CHARACTER
099B 963 :
099B 964 OUTPUT:
099B 965 R8 - INPUT CHARACTER
099B 966 R9 - BUFFER POINTER UPDATED (BUFFER IN ASCIIZ FORMAT)
099B 967 :
099B 968
099B 969 GETCHAR:
099B 970 MOVZBL (R9)+,R8 : GET NEXT CHARACTER
099B 971 BEQL 10$ : READ IF NONE AVAIL
099E 972 RSB
09A0 973 10$: MOVAB INBUF-B(R11),R9 : SET ADDRESS OF INPUT BUFFER
09A1 974 .IF NDF,SW_PROCESS
09A5 975 20$: TSTL AP : CHECK FOR CONSOLE
09A5 976 BNEQ 30$ YES
09A5 977 JSB G^CONSGETCHAR : GET A CHARACTER FROM THE CONSOLE TERMINAL
09A5 978 MOVB R0,R8
09A5 979 BRB 60$ : CONTINUE IN COMMON
09A5 980 30$: MOVW RDCR(AP),R0 : GET STATUS
09A5 981 40$: BBC #7,R0,30$ : WAIT FOR READY
09A5 982 MOVB RDBUF(AP),R8 : GET CHARACTER
09A5 983 BRB 60$ : MERGE WITH COMMON
09A5 984 .IFF FALSE IF PROCESS VERSION
09A5 985 15$: MOVAL TTITMLST,R0 : get the relocateable address
09AA 986 $QIOW_S EFN=#31,-
09AA 987 CHAN=TTCHAN,- : INPUT DEVICE CHANNEL
09AA 988 IOSB=TTIOSB,- : IO STATUS BLOCK
09AA 989 FUNC=#<IOS_READVBLK!IOSM_EXTEND>,- :
09AA 990 P1=(R9),- : BUFFER ADDRESS
09AA 991 P2=#80,- : READ SIZE
09AA 992 P5=R0,-
09AA 993 P6=TTITMLSTLEN
09D1 994 CMPW R0,#SS$_INSFMEM : If any resource error occurs,
09D6 995 BEQL 760$ : wait for an I/O completion
09DB 996 CMPW R0,#SS$_EXQUOTA : and try again.
09DD 997 BEQL 760$
09E2 998 CMPW R0,#SS$_EXQUOTA$TRT
09E4 999 BLSSU 790$
09E9 1000 CMPW R0,#SS$_EXQUOTAEND
09EB 1001 BGTRU 790$
09F4 1002 760$: SWAITFR_S EFN=#31
09F6 1003 BRB -15$
09F6 1004 790$:
09F6 1005 MOVZWL TTIOSB+2,R0 : GET SIZE READ
09FB 1006 MOVB TTIOSB+4,(R0)+[R9] : BUFFER TERMINATOR
0A01 1007 CLRB (R9)[R0] : MARK END OF BUFFER
0A04 1008 MOVL R9,R2 : POINT TO START OF STRING
0A07 1009 20$: MOVZBL (R2)+,R8 : GET A CHARACTER
0A0C 1010 BEQL 15$ : EMPTY, READ SOME MORE
0A0C 1011 .ENDC
0A0C 1012 60$: BICB #^X80,R8 : STRIP PARITY
0A10 1013 CMPB R8,#RUBOUT : CHECK FOR RUBOUT
0A14 1014 BNEQ 90$ : NO
0A16 1015 BBSS #V_RUB,(R10),70$ : SET START OF RUBOUT SEQUENCE
```



	FF1C	30	0A1A	1016	BSBW	OUTBSLSH	: OUTPUT BACK SLASH
58	79	9A	0A1D	1017	MOVZBL	-(R9),R8	: GET RUBBED OUT CHAR
	04	12	0A20	1018	BNEQ	80\$	: SKIP INC
	59	D6	0A22	1019	INCL	R9	: POINT AT START OF BUFFER
	E1	11	0A24	1020	BRB	20\$	: AND GET ANOTHER
	FF16	30	0A26	1021	BSBW	OUTR8	: OUTPUT RUBBED OUT CHAR
	DC	11	0A29	1022	BRB	20\$	: AND GET ANOTHER
03 6A	06	E5	0A2B	1023	BBCC	#V RUB,(R10),100\$	: TERMINATE RUBOUT SEQUENCE
	FF07	30	0A2F	1024	BSBW	OUTBSLSH	: OUTPUT BACK SLASH
03 58	06	E1	0A32	1025	BBC	#6,R8,110\$	: BR IF NOT ALPHA
58	20	8A	0A36	1026	BICB	#32,R8	: SET TO UPPER CASE
			0A39	1027			: .
			0A39	1028	.IF	NDF,SW_PROCESS	: .
			0A39	1029	BSBW	OUTR8	: ECHO CHARACTER
			0A39	1030	.ENDC		: .
FAFO CF	89 58	90	0A39	1031	MOVB	R8,(R9)+	: BUFFER NEW CHAR
	0A 58	3A	0A3C	1032	LOCC	R8,#TERM,TERM	: CHECK FOR TERMINATOR
	C3	13	0A42	1033	BEQL	20\$	: NOT A TERMINATOR
	58 0D	91	0A44	1034	CMPB	#CR,R8	: IS CHAR = RETURN
	03	12	0A47	1035	BNEQ	120\$	: NO
	FF45	30	0A49	1036	BSBW	CRLF	: YES, SEND CR/LF
	69	94	0A4C	1037	CLRB	(R9)	: MARK END OF BUFFER
59 84 AB	9E	0A4E	1038	MOVAB	INBUF-B(R11),R9		: RESTORE BUFFER BASE
	FF46	31	0A52	1039	BRW	GETCHAR	: AND TRY AGAIN

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      0A55 1041 .SBTTL PLUS/MINUS OPERATORS
      0A55 1042 :
      0A55 1043 : PLUS/MINUS OPERATORS
      0A55 1044 :
      0A55 1045 :
      0A55 1046 : BLANK:
      0A55 1047 : OPERATOR: : SAME AS PLUS
      0A58 1048 : BSBW ENDEXPR : END EXPR
      0A5D 1049 : SUBB3 #OPERBAS,R0,OPER-B(R11) : SET OPERATOR
      0A5E 1050 : RSB : RETURN
      0A5E 1051 :
      0A5E 1052 : MONADIC MINUS - NEGATE
      0A5E 1053 :
      0A60 1054 : NEGATE: TSTL R6 : TEST ACCUMULATOR
      0A62 1055 : BEQL 58 : EMPTY
      0A65 1056 : BSBW ENDEXPR : OTHERWISE PERFORM OPERATION
      0A69 1057 : 5$: XORB #<1@V_NEGATE>,(R10) : TOGGLE NEGATE FLAG
      0A6A 1058 : 10$: RSB : AND RETURN
      0A6A 1059 :

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      0A6A 1061
      0A6A 1062
      0A6A 1063
      0A6A 1064
      0A6A 1065 TAB:
      0A6E 1066
      0A73 1067
      0A78 1068
      0A7A 1069
      0A7A 1070
      0A7A 1071
      0A7A 1072
      0A7A 1073
      0A7A 1074 ESCAP:
      0A7A 1075
      0A7E 1076
      0A81 1077
      0A83 1078
      0A85 1079
      0A8A 1080 10$:
      0A8D 1081 LOCP:

50 6A 01 04 AB DO 0A6A 1061
6A 01 1F 50 EF 0A6A 1062
13 11 FO 0A6E 1063
11 11 FO 0A73 1064
11 11 FO 0A78 1065
11 11 FO 0A7A 1066
11 11 FO 0A7A 1067
11 11 FO 0A7A 1068
11 11 FO 0A7A 1069
11 11 FO 0A7A 1070
11 11 FO 0A7A 1071
11 11 FO 0A7A 1072
11 11 FO 0A7A 1073
11 11 FO 0A7A 1074
11 11 FO 0A7A 1075
11 11 FO 0A7E 1076
11 11 FO 0A81 1077
11 11 FO 0A83 1078
11 11 FO 0A85 1079
11 11 FO 0A8A 1080
11 11 FO 0A8D 1081

      .SBTTL TAB - INDIRECT DISPLAY
      TAB
      MOVL QUAN-B(R11),CURDOT-B(R11) ; GO INDIRECT
      EXTZV #V_PMODE,#1,(R10),R0 ; GET PROCESSOR REGISTER MODE
      INSV R0,#V_PREG,#1,(R10) ; AND COPY TO SEMI-PERMANENT FLAG
      BRB LOCP ; AND DISPLAY IT

      ESCAPE - DISPLAY PREVIOUS LOCATION

      BBS #V_INSTR,(R10),LOCP ; BRANCH IF INSTRUCTION MODE
      MOVL #1,R1 ; ASSUME UNIT INCREMENT
      TSTL (R10) ; CHECK FOR PROCESSOR REGISTER
      BLSS 10$ ; YES, USE UNIT INCREMENT
      ROTL CURTYPE-B(R11),R1,R1 ; FORM INCREMENT
      SUBL R1,CURDOT-B(R11) ; AND SUBTRACT FROM DOT
      BRW LOCPROMPT ; PROMPT WITH CONTENT
```

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0A90 1083 .SBTTL DISPLAY INSTRUCTION RANGE
0A90 1084 :
0A90 1085 :
0A90 1086 :
0A90 1087 INSTR: BSBW ENDFIELD ; TERMINATE FIELD
0A93 1088 BICB #10V ASCII, (R10) ; CLEAR CHARACTER DISPLAY MODE
0A96 1089 BBS #V FT, (R10), 5$ ; ADDRESS SPECIFIED?
0A9A 1090 MOVL QUAN-B(R11), CURDOT-B(R11) ; EXAMINE AT Q IF UNSPECIFIED
0A9E 1091 BRB 10$
0AA0 1092 5$: MOVL F1-B(R11), CURDOT-B(R11) ; IF ADDRESS SPECIFIED, SET NEW DOT
0AA4 1093 10$: BLSW #10V INSTR, (R10) ; SET INSTRUCTION DISPLAY MODE
0AA9 1094 BSBW OUTINS ; DISPLAY INSTRUCTION
0AAC 1095 BBC #V F2, (R10), 30$ ; IF NO RANGE SPECIFIED, EXIT
0AB0 1096 20$: CMPL F2-B(R11), CURDOT-B(R11) ; END OF RANGE?
0AB4 1097 BLEQ 30$ ; BRANCH IF DONE
0AB6 1098 BSBW NEXTLOC ; OUTPUT NEXT INSTRUCTION
0AB9 1099 BRB 20$ ; LOOP UNTIL DONE
0ABB 1100 30$: BRB RESET ; RESET SCANNER
```



[illegible]

Address	Hex	Char	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
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				0B03	1155	.SBTTL	LEFT BRACKET - MODE SELECTION	
				0B03	1156	:		
				0B03	1157	:		
				0B03	1158	:		
				0B03	1159	:		
				0B03	1160	Modes:		: MODE CHARACTER LIST
		49		0B03	1161	.ASCII	/I/	: INSTRUCTION MODE, VARIABLE LENGTH
		43		0B04	1162	.ASCII	/C/	: CHARACTER, CURRENT LENGTH
		4C		0B05	1163	.ASCII	/L/	: LONG, HEX
		57		0B06	1164	.ASCII	/W/	: WORD, HEX
		42		0B07	1165	.ASCII	/B/	: BYTE, HEX
		00000005		0B08	1166	NMODES=.	-MODES	: NUMBER OF MODE CHARACTERS
				0B08	1167			
				0B08	1168			
				0B08	1169	LBRACKET:		: MODE SELECTION
F3 AF	05	FE90	30	0B08	1170	BSBW	GETCHAR	: GET MODE CHAR
		58	3A	0B08	1171	LOCC	R8,#NMODES,MODES	: CONVERT TO INDEX
		EE	13	0B10	1172	BEQL	ERR2	: NOT FOUND, ERROR
	04	50	D1	0B12	1173	CMLP	R0,#4	: CHECK FOR 'C'
		10	13	0B15	1174	BEQL	10\$	: BRANCH IF 'C'
		17	14	0B17	1175	BGTR	20\$	: BRANCH IF 'I'
FE AB	50	01	83	0B19	1176	SUBB3	#1,R0,CURTYPE-B(R11)	: SET MODE
6A	2000	8F	AA	0B1E	1177	BICW	#12V_INSTR,(R10)	: CLEAR INSTRUCTION DISPLAY MODE
	6A	02	8A	0B23	1178	BICB	#<12V_ASCII>,(R10)	: CLEAR CHARACTER DISPLAY MODE
			05	0B26	1179	RSB		: RETURN
	6A	02	88	0B27	1180	BISB	#<12V_ASCII>,(R10)	: SET CHARACTER DISPLAY MODE
6A	2000	8F	AA	0B2A	1181	BICW	#12V_INSTR,(R10)	: CLEAR CHARACTER DISPLAY MODE
			05	0B2F	1182	RSB		
6A	2000	8F	A8	0B30	1183	BISW	#12V_INSTR,(R10)	: SET INSTRUCTION DISPLAY MODE
		EC	11	0B35	1184	BRB	5\$	

DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
SINGLE STEP

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6A	02	03	01	F0	0B37	1186	. <td>SINGLE STEP</td> <td></td>	SINGLE STEP	
6A	80008000	8F	CA	04	0B37	1187	:	STEP	
					0B37	1188	:		
					0B37	1189	:		
					0B37	1190	STEP:	INSV	#1,#V TBIT,#2,(R10) ; CLR V ATBRK, SET V TBIT
					0B3C	1191		BICL	#<<12V_PMODE>>!<12V_PREG>>,(R10) ; CLEAR PROCESSOR REGISTER M
					0B43	1192		RET	; AND RETURN



```
                                .SBTTL STEPOVER - STEP OVER ROUTINE CALL
                                STEPOVER
                                STEPOVER:
6A  50  54 BB  9A 0B44 1194 MOVZBL 8SAVPC-B(R11),R0      : GET NEXT INSTRUCTION TO EXECUTE
    80008000 8F CA 0B44 1195 BICL  #<<12V_PMODE>!!<12V_PREG>>, (R10) : CLEAR PROCESSOR REGISTER R
    51  F9C1 CF  9E 0B48 1200 MOVAB  OVEROPCODES,R1      : ADDRESS OF LIST OF OPCODES
    52  05  9A 0B4F 1201 MOVZBL  #OVEROPCLEN,R2      : SIZE OF TABLE
    81  50  91 0B54 1202 CMPB  R0,(R1)+      : MATCH?
    05  13  0B5A 1203 BEQL  20$      : BRANCH IF FOUND
    FB 52  F5 0B5A 1204 SOBGTR R2,10$      : LOOP UNTIL FOUND
    D6 11  0B5C 1205 BRB  STEP      : IF NOT A ROUTINE CALL, NORMAL STEP
52  00000000 GF 9E 0B5F 1206 MOVAB  G^LIB$INS_DECODE,R2 : GET ADDRESS OF FOLLOWING INSTRUCTION
    2B 13  0B61 1207 BEQL  30$      : IF NOT AVAILABLE, ERROR
    54 AB DD 0B68 1208 PUSH  SAVPC-B(R11)      : COPY ADDRESS OF INSTRUCTION STREAM
    00 DD 0B6A 1209 PUSHL  #0      : PUSH NULL DESCRIPTOR
    SE DD 0B6D 1210 PUSHL  SP      : ADDRESS OF OUTPUT DESCRIPTOR
    08 AE DF 0B6F 1211 PUSHAL 8(SP)      : ACCESS INSTRUCTION STREAM DIRECTLY
    62 02 FB 0B71 1212 CALLS  #2,(R2)      : FIND ADDRESS OF FOLLOWING INSTRUCTION
    15 50 E9 0B74 1213 BLBC  R0,25$      : IF NOT INTERPRETABLE, ERROR
    0B7A 1214 .IF  DF,SW_PROCESS      :
    54 04 AE D0 0B7A 1215 MOVL  4(SP),R4      : GET ADDRESS OF NEXT INSTRUCTION
    55 54 D0 0B7E 1216 MOVL  R4,R5      : MAKE END=START
    0B3F 30 0B81 1217 BSBW  SETWRT      : MAKE INSTRUCTION WRITABLE
    03 BA 0B84 1218 .ENDC      :
    61 61 90 0B84 1219 POPR  #^M<R0,R1>      : GET UPDATED STREAM POINTER
    F832 CF 51 D0 0B86 1220 MOVB  (R1),(R1)      : ERROR IF UNABLE TO WRITE BREAKPOINT
    04 0B88 1221 MOVL  R1,OVRADR      : SET TEMPORARY BREAKPOINT
    0B8E 1222 RET      : START EXECUTION
    0B8F 1223      :
    SE 0B  C0 0B8F 1224 ADDL  #8,SP      : CLEAN STACK
    F9C1 31 0B92 1225 BRW  ERROR      : REPORT ERROR - UNABLE TO STEP OVER
    25$:
    30$:
```

```
0B95 1228 .SBTTL BRKPOINT - SET/CLEAR BREAKPOINTS
0B95 1229
0B95 1230 BRKPOINT
0B95 1231
0B95 1232 BRKPOINT:
0B95 1233 BBC #V_F1,(R10),SHOBRK
0B99 1234 BBS #V_F2,(R10),208
0B9D 1235 MOVL #1,R2
0BA0 1236 10$: TSTL BRKADR[R2]
0BA5 1237 BEQL 308
0BA7 1238 ACBL #NBRK,#1,R2,10$
0BAD 1239 15$: BRW ERROR
0BB0 1240 20$: MOVL F2-B(R11),R2
0BB4 1241 BEQL 108
0BB6 1242 CMPL #NBRK,R2
0BB9 1243 BLSS 15$
0BBB 1244 30$: CLRL BRKDSP[R2]
0BC0 1245 CLRL BRKCOM[R2]
0BC5 1246 MOVL F1-B(R11),R0
0BC9 1247 BEQL 358
0BCB 1248 .IF DF,SW PROCESS
0BCB 1249 PUSHM #*M<R0,R1,R2,R3,R4,R5>
0BCD 1250 MOVL R0,R4
0BD0 1251 MOVL R0,R5
0BD3 1252 BSBW SETWRT
0BD6 1253 MOVL (SP),R0
0BD9 1254 .ENDC
0BD9 1255 MOVB (R0),(R0)
0BDC 1256 .IF DF,SW PROCESS
0BDC 1257 BSBW REPROT
0BDF 1258 POPR #*M<R0,R1,R2,R3,R4,R5>
0BE1 1259 .ENDC
0BE1 1260 35$: BBC #V_F3,(R10),408
0BE5 1261 MOVL F3-B(R11),BRKDSP[R2]
0BEC 1262 BEQL 408
0BEE 1263 TSTL @F3-B(R11)
0BF1 1264 40$: BBC #V_F4,(R10),458
0BF5 1265 MOVL F4-B(R11),BRKCOM[R2]
0BFC 1266 45$: MOVL R0,BRKADR[R2]
0C02 1267 BRW RESET
0C05 1268
0C05 1269 SHOBRK
0C05 1270
0C05 1271 SHOBRK:
0C05 1272 MOVL #1,R5
0C08 1273 10$: MOVL BRKADR[R5],R8
0C0E 1274 BEQL 208
0C10 1275 MOVL R5,R3
0C13 1276 BSBW CRLF
0C16 1277 BSBW OUTDIGIT
0C19 1278 BSBW OUTSPACE
0C1C 1279 MOVL R8,R3
0C1F 1280 BSBW OUTLONG
0C22 1281 BSBW OUTSPACE
0C25 1282 MOVL BRKDSP[R5],R3
0C28 1283 BEQL 15$
0C2D 1284 BSBW OUTLONG

6C 6A 08 E1 0B95 1228
13 6A 09 E0 0B99 1234
52 01 D0 0B9D 1235
F7F7 CF42 D5 0BA0 1236
14 13 0BA5 1237
FFF3 52 01 08 F1 0BA7 1238
F9A6 31 0BAD 1239
52 DC AB D0 0BB0 1240
EA 13 0BB4 1241
52 08 D1 0BB6 1242
F2 19 0BB9 1243
F809 CF42 D4 0BBB 1244
F824 CF42 D4 0BC0 1245
50 D8 AB D0 0BC5 1246
16 13 0BC9 1247
3F BB 0BCB 1248
54 50 D0 0BCD 1250
55 50 D0 0BD0 1251
07ED 30 0BD3 1252
50 6E D0 0BD6 1253
60 60 90 0BD9 1255
081B 30 0BDC 1257
3F BA 0BDF 1258
OC 6A 0A E1 0BE1 1259
F7DD CF42 E0 AB D0 0BE5 1261
03 13 0BEC 1262
E0 BB D5 0BEE 1263
07 6A 08 E1 0BF1 1264
F7ED CF42 E4 AB D0 0BF5 1265
F79A CF42 50 D0 0BFC 1266
FEC7 31 0C02 1267
OC05 1268
OC05 1269
OC05 1270
OC05 1271
58 55 01 D0 0C05 1272
F78F CF45 D0 0C08 1273
2E 13 0C0E 1274
53 55 D0 0C10 1275
FD7B 50 0C13 1276
FCAB 50 0C16 1277
FD70 50 0C19 1278
53 58 D0 0C1C 1279
FCA6 30 0C1F 1280
FD67 50 0C22 1281
53 F79F CF45 D0 0C25 1282
03 13 0C28 1283
FC98 30 0C2D 1284
```

DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
BRKPOINT - SET/CLEAR BREAKPOINTS

M 16

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53	F7B4	CF45	D0	OC30	1285	158:	MOVL	BRKCOM[R5],R3	:	GET COMMAND STRING ADDRESS
		06	13	OC36	1286		BEQL	208	:	NONE
		FD51	30	OC38	1287		BSBW	OUTSPACE	:	SPACE ANOTHER
		FC8A	30	OC3B	1288		BSBW	OUTLONG	:	AND OUTPUT A LONGWORD
FFC4 55	01	08	F1	OC3E	1289	208:	ACBL	#NBRK,#1,R5,108	:	DO THEM ALL
		FD4A	31	OC44	1290		BRW	CRLF	:	AND EXIT THROUGH CRLF

```

      0C47 1292      .SBTTL GO - START EXECUTION AT SPECIFIED LOCATION
      0C47 1293      GO
      0C47 1294      GO
      0C47 1295      GO
      05 6A 08 E1 0C47 1296 GO: BBC #V F1,(R10),PROCEED ; JUST PROCEED IF NO VALUE
54 AB DB AB D0 0C48 1297      MOVL F1-B(R11),SAVPC-B(R11) ; SET NEW PC
      0C50 1298      BRW PROCEED ; FALL INTO PROCEED
      0C50 1299      PROCEED
      0C50 1300      PROCEED
      0C50 1301      PROCEED
      0C50 1302      PROCEED:
6A 80008000 8F CA 0C50 1303      BICL #<<12V_PMODE>!!<12V_PREG>>,(R10) ; CLEAR PROCESSOR REGISTER M
      04 0C57 1304      RET ; RETURN

```



[illegible]

```
OCEB 1363      .IF      NDF,SW_PROCESS
OCEB 1364      .TSTL     AP
OCEB 1365      .BNEQ     ERR3      : CHECK FOR SIMULATOR
OCEB 1366      :                                     :
OCEB 1367      :                                     :
OCEB 1368      CPUDISP <<780,CLR_780>,-      : *DISPATCH ON CPU TYPE*
OCEB 1369      <<750,CLR_750>,-
OCEB 1370      <<730,CLR_730>,-
OCEB 1371      <<790,CLR_790>,-
OCEB 1372      <<UV1,CLR_UV1>>,-
OCEB 1373      ENVIRON=XDELTA;
OCEB 1374      :
OCEB 1375      CLR_780:
OCEB 1376      MFPR      #PR$ SBIFS, -(SP)
OCEB 1377      BBCC      #25, TSP, 10$
OCEB 1378      10$:      MTPR      (SP)+, #PR$ SBIFS
OCEB 1379      BRB       CLR_END
OCEB 1380      :
OCEB 1381      CLR_UV1:
OCEB 1382      CLR_730:
OCEB 1383      CLR_750:
OCEB 1384      MTPR      #^XF, #PR$ MCESR
OCEB 1385      BRB       CLR_END
OCEB 1386      CLR_790:
OCEB 1387      MFPR      #PR$ EHSR, -(SP)
OCEB 1388      BBCC      #6, (SP), 10$
OCEB 1389      10$:      MTPR      (SP)+, #PR$ EHSR
OCEB 1390      JSB       SYS$CLRSBIA
OCEB 1391      :
OCEB 1392      CLR_END:
OCEB 1393      :
OCEB 1394      :
OCEB 1395      .ENDC
OCEB 1396      10$:
F86B 31 OCEB 1397 ERR3:  BRW      ERROR      : AND DECLARE ERROR
OCEB 1398
```

```

OCEB 1400 .SBTTL REGISTER SAVE AND RESTORE
OCEB 1401
OCEB 1402
OCEB 1403
OCEB 1404
OCEB 1405 SAVE:
OCEB 1406
OCEB 1407
OCEB 1408
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OCEB 1454
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OCEB 1456

51 51 02 18 9F 0CF4 1413
51 51 00E4 8F A4 0CF6 1414
51 F39D CF41 9E 0CF8 1415
50 08 AC D0 0CFD 1416
81 0C A0 7D 0D02 1417
81 52 7D 0D08 1418
81 54 7D 0D0C 1419
81 56 7D 0D10 1420
81 58 7D 0D11 1421
81 5A 7D 0D13 1422
81 5C 7D 0D16 1423
81 5E 7D 0D19 1424
81 60 7D 0D1C 1425
81 62 7D 0D1F 1426
81 64 7D 0D1F 1427
81 66 7D 0D1F 1428
81 68 7D 0D1F 1429
81 6A 7D 0D1F 1430
81 6C 7D 0D1F 1431
81 6E 7D 0D23 1432
81 70 7D 0D28 1433
81 72 7D 0D2D 1434
81 74 7D 0D31 1435
81 76 7D 0D34 1436
81 78 7D 0D34 1437
81 7A 7D 0D34 1438
81 7C 7D 0D34 1439
81 7E 7D 0D34 1440
81 80 7D 0D34 1441
81 82 7D 0D34 1442
81 84 7D 0D34 1443
81 86 7D 0D34 1444
81 88 7D 0D34 1445
81 8A 7D 0D34 1446
81 8C 7D 0D34 1447
81 8E 7D 0D34 1448
81 90 7D 0D34 1449
81 92 7D 0D34 1450
81 94 7D 0D34 1451
81 96 7D 0D34 1452
81 98 7D 0D34 1453
81 9A 7D 0D34 1454
81 9C 7D 0D34 1455
81 9E 7D 0D34 1456

20$:
5B FFA4 C1 9E 0D34 1446
60 AB 8E D0 0D39 1447
5A D4 AB 9E 0D3D 1448
59 84 AB 9E 0D41 1450
69 94 9E 0D45 1451
6A 96 9E 0D47 1452
6C 98 9E 0D47 1453
6E 9A 9E 0D47 1454
68 9C 9E 0D47 1455
6A 9E 9E 0D47 1456

.NDF,SW_PROCESS
SETIPL #31
JSB INISWRITABLE
MOVQ R0,SAVREG
MOVAB SAVR2,R1
.IFF
$SETAST_S #0
PUSHAB -(R0)
MOVPSL R1
EXTZV #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1
MULW #CONTEXTSZ,R1
MOVAB SAVREG[R1],R1
MOVL 8(AP),R0
MOVQ 12(R0),(R1)+
.ENDC
MOVQ R2,(R1)+
MOVQ R4,(R1)+
MOVQ R6,(R1)+
MOVQ R8,(R1)+
MOVQ R10,(R1)+
.IFF
.NDF,SW_PROCESS
MOVQ AP,(R1)+
MOVAB 12(SP),(R1)+
MOVQ 4(SP),(R1)+
.IFF
MOVQ 8(FP),(R1)+
SUBL3 #1,84(AP),R0
MOVAL 84(AP)[R0],R0
MOVAL 8(R0),(R1)+
MOVQ (R0),(R1)+
.ENDC
.NDF,SW_PROCESS
MOVL R1,R2
JSB G^CONSONCTY
MOVL R0,(R2)+
MOVL R1,(R2)+
MOVL R2,R1
CLRL AP
MOVAB B,R11
.IFF
W^<B-<SAVPSL+4>>(R1),R11
MOVL (SP)+,ASTEN-B(R11)
.ENDC
MOVAB STATUS-B(R11),R10
MOVAB INBUF-B(R11),R9
CLRB (R9)
.IFF
.NDF,SW_PROCESS
BSBW GETSCB
MOVL 4(R0),MCHKSAV
MOVAB MCHK,4(R0)
MOVAB XDELACV,*X20(R0)

DISABLE
MAKE THE SYSTEM WRITABLE
SAVE R0,R1
SETUP BASE FOR REMAINING REGS
FALSE IF PROCESS VERSION
DISABLE ASTS
SAVE ENABLE VALUE-1
GET CURRENT PSL
ISOLATE CURRENT MODE
COMPUTE OFFSET TO PROPER CONTEXT AREA
FORM ADDRESS OF REGISTER SAVE
GET POINTER TO MECHANISM
SAVE R0,R1
SAVE R2,R3
SAVE R4,R5
SAVE R6,R7
SAVE R8,R9
SAVE R10,R11
SAVE AP,FP
ASSUME KERNEL STACK
SAVE PC,PSL
SAVE AP,FP
GET NUMBER OF ARGS IN SIGNAL
POINT TO PC,PSL
COMPUTE SP
SAVE PC,PSL
SAVE R1
ALLOCATE THE CONSOLE TERMINAL
SAVE CONSOLE TRANSMIT STATUS
SAVE CONSOLE RECVR STATUS
RESTORE R1
ZAP DEVICE ADDRESS BASE
AND DATA BASE ADDRESS
FALSE FOR PROCESS VERSION
SET BASE OF CONTEXT AREA
SAVE AST ENABLE
SET STATUS BASE
POINT TO INPUT BUFFER
MAKE BUFFER EMPTY
GET BASE OF SCB
SAVE ORIGINAL MCHK VECTOR
SET TO XDELTA VECTOR
SET ACCESS VIOLATION VECTOR
```

			OD47	1457	MOVAB	XDELACV,*X24(R0)	: SET PG FAULT VECTOR
			OD47	1458	MOVAB	XDELACV,*X18(R0)	: SET RESERVED OPERAND HANDLER
			OD47	1459	EXTZV	#PSLSV_CURMOD,#PSLSS_CURMOD,8(SP),R0	: GET MODE
			OD47	1460	BEQL	30\$	: CORRECT ALREADY IF KERNEL
			OD47	1461	ADDL	#PRS_KSP,R0	: COMPUTE PROCESSOR REGISTER
			OD47	1462	MFPFR	R0,SAVSP-B(R11)	: AND SAVE CORRECT SP
			OD47	1463	.ENDC		
			OD47	1464	30\$: BRW	RESET	: RESET SCANNER
			OD4A	1465			
			OD4A	1466	:		
			OD4A	1467	:		
			OD4A	1468	:		
			OD4A	1469	RESTORE:		RESTORE - RESTORE TARGET REGISTERS
			OD4A	1470	.IF	NDF,SW_PROCESS	: RESTORE EVERYTHING
			OD4A	1471	MOVQ	SAVPC-B(R11),4(SP)	: SET PC,PSL
			OD4A	1472	.IFF		: FALSE IF PROCESS
			OD4A	1473	SUBL3	#1,24(AP),R0	: GET SIGNAL ARG COUNT
			OD4F	1474	MOVAL	24(AP)[R0],R0	: COMPUTE ADDRESS OF PC,PSL
			OD54	1475	MOVQ	SAVPC-B(R11),(R0)	: STORE UPDATED PC,PSL
			OD58	1476	.ENDC		
			OD58	1477	RESTORR:		RESTORE REGISTERS ONLY
			OD58	1478	.IF	NDF,SW_PROCESS	: RESTORE REGISTERS ONLY
			OD58	1479	BSBB	GETSCB	: GET BASE OF SCB
			OD58	1480	MOVAB	EXESACVIOLAT,*X20(R0)	: RESTORE ACCESS VECTOR
			OD58	1481	MOVAB	MMG\$PAGEFAULT,*X24(R0)	: AND PAGE FAULT VECTOR
			OD58	1482	MOVL	MCHKSAV,4(R0)	: RESTORE MACHINE CHECK VECTOR
			OD58	1483	MOVAB	EXESROPAND,*X18(R0)	: RESTORE RESERVED OPERAND VECTOR
			OD58	1484	TSTW	AP	: CHECK FOR CONSOLE
			OD58	1485	BNEQ	10\$	: NO, OTHER DEVICE
			OD58	1486	MOVL	SAVOCR-B(R11),R0	: RESTORE INITIAL TX STATUS
			OD58	1487	MOVL	SAVRXCS-B(R11),R1	: AND INITIAL RECEIVER STATE
			OD58	1488	JSB	G^CONSRELEASECTY	
			OD58	1489	BRB	20\$	: MERGE WITH COMMON CODE
			OD58	1490			
			OD58	1491	10\$: MOVW	SAVOCR-B(R11),OUTCR(AP)	: RESTORE OUTPUT CSR
			OD58	1492	MOVW	SAVRCR-B(R11),RDCR(AP)	: AND INPUT CSR CONTENT
			OD58	1493	.IFF		
			OD58	1494	PUSHL	ASTEN-B(R11)	: SAVE AST ENABLE
			OD58	1495	.ENDC		
			OD5B	1496	20\$: MOVAB	SAVR2-B(R11),R1	: SET BASE FOR RESTORE
			OD5F	1497	MOVQ	(R1)+,R2	: RESTORE R2,R3
			OD62	1498	MOVQ	(R1)+,R4	: RESTORE R4,R5
			OD65	1499	MOVQ	(R1)+,R6	: RESTORE R6,R7
			OD68	1500	MOVQ	(R1)+,R8	: RESTORE R8,R9
			OD68	1501	MOVQ	(R1)+,R10	: RESTORE R10,R11
			OD6E	1502	.IF	NDF,SW_PROCESS	
			OD6E	1503	MOVQ	(R1)+,AP	: RESTORE AP,FP
			OD6E	1504	MOVQ	SAVREG,R0	: RESTORE R0,R1
			OD6E	1505	.IFF		: FALSE IF PROCESS VERSION
			OD6E	1506	MOVQ	(R1)+,8(FP)	: SET NEW VALUES FOR AP,FP
			OD72	1507	MOVL	8(AP),R0	: GET MECHANISM POINTER
			OD76	1508	MOVQ	<SAVREG-SAVSP>(R1),12(R0)	: STORE UPDATED R0,R1
			OD7B	1509	MOVPSL	R1	: GET CURRENT PSL
			OD7D	1510	EXTZV	#PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1	: GET CURRENT MODE
			OD82	1511	BBCC	R1,DBG\$ACTIVE,30\$	: CLEAR ACTIVE BIT FOR MODE
			OD88	1512	30\$: TSTL	(SP)+	: CHECK FOR AST ENABLE
			OD88	1513			

50 04 BC 01 C3  
50 04 BC40 DE  
60 54 AB 7D

60 AB DD

51 20 AB 9E  
52 81 7D  
54 81 7D  
56 81 7D  
58 81 7D  
5A 81 7D

08 AD 81 7D  
50 08 AC DO  
OC AO C8 A1 7D  
51 DC  
51 02 18 EF  
00 F74D CF 51 E5

BE DS



09	13	OD8A	1514	BEQL	358	:	NO
		OD8C	1515	\$SETAST_S	#1	:	RE- ENABLE AST RECOGNITION
		OD95	1516	358:		:	
		OD95	1517	.ENDC		:	
		OD95	1518	.IF	NDF SW PROCESS	:	
		OD95	1519	JSB	INI\$RDONLY	:	REPROTECT THE SYSTEM CODE
		OD95	1520	.ENDC		:	
05		OD95	1521	RSB		:	AND RETURN

```
0D96 1524      .SBTTL  GET SCB ADDRESS
0D96 1525
0D96 1526      :
0D96 1527      : SUBROUTINE GETSCB IS CALLED TO GET THE PHYSICAL OR VIRTUAL
0D96 1528      : ADDRESS OF THE CURRENT SCB.
0D96 1529      :
0D96 1530      : INPUTS:      NONE
0D96 1531      :
0D96 1532      : OUTPUTS:     R0 = SCB ADDRESS
0D96 1533      :              OTHER REGISTERS PRESERVED
0D96 1534      :
0D96 1535      :
0D96 1536      : .IF      NDF,SW PROCESS      : NOT FOR PROCESS VERSION
0D96 1537 GETSCB: MFPR    #PR$_MAPEN,R0      : GET MAPPING STATUS
0D96 1538      BNEQ     10$      : BRANCH IF MAPPING ENABLED
0D96 1539      MFPR    #PR$_SCBB,R0      : ELSE GET PHY ADDR OF SCB
0D96 1540      BRB      20$      : JOIN COMMON RETURN
0D96 1541 10$:    MOVL    EXESGL_SCB,R0      : IF MAPPING ENABLED, GET SCB VA
0D96 1542 20$:    RSB
0D96 1543      .ENDC      :
```

```
00 20 54 41 20 4B 52 42 20
                                0D96 1545 .SBTTL BPT TRAP HANDLER
                                0D96 1546 :
                                0D96 1547 : HANDLE BREAKPOINT TRAPS
                                0D96 1548 :
                                0D96 1549 MSG: .ASCIZ / BRK AT / : BREAK POINT MESSAGE
                                0D9F 1550 .ALIGN LONG : LONGWORD ALIGNMENT
                                0DA0 1551 .IF NDF,SW_PROCESS : EXEC VERSION
                                0DA0 1552 XDELBPT: : XDELTA BPT ENTRY
                                0DA0 1553 .IFF :
                                0DA0 1554 XDELBPT: : DELTA BPT ENTRY
                                0DA0 1555 .ENDC :
                                0DA0 1556 BSBW SAVE : SAVE REGS AND DISABLE
                                0DA3 1557 BSBW GETBPTX : GET INDEX OF BPT
                                0DA6 1558 TSTL R3 : CHECK FOR MATCH
                                0DA8 1559 BNEQ 10$ : YES, FOUND IT
                                0DAA 1560 CMPL SAVPC-B(R11),OVRADR : IS THIS A TEMPORARY BREAKPOINT?
                                0DB0 1561 BEQL 20$ : BRANCH IF SO
                                0DB2 1562 BSBW RESTORR : RESTORE REGISTERS ONLY
                                0DB5 1563 .IF NDF,SW_PROCESS :
                                0DB5 1564 MOVZBL 6(SP),=(SP) : GET IPL
                                0DB5 1565 ENBINT : ENABLE
                                0DB5 1566 JMP EXESBREAK : AND HANDLE NORMALLY
                                0DB5 1567 .IFF : FALSE IF PROCESS VERSION
                                0DB5 1568 :
                                0DB5 1569 : ***** UNEXPECTED BREAKPOINT *****
                                0DB5 1570 CLRL R0 : RETURN FALSE
                                0DB7 1571 RET :
                                0DB8 1572 .ENDC :
                                0DB8 1573 :
                                0DB8 1574 : WE JUST HIT A TEMPORARY BREAKPOINT SET FROM A STEP-OVER
                                0DB8 1575 :
                                0DB8 1576 20$: BSBW UNBRK : RESTORE OPCODES, INCLUDING TEMP BRKPT
                                0DB8 1577 CLRL OVRADR : REMOVE TEMPORARY BREAKPOINT
                                0DBF 1578 BRB OUTPC : AND PRETEND WE JUST STEPPED
                                0DC1 1579 :
                                0DC1 1580 10$: BISB #<<12V_TBIT>!<12V_ATBRK>>,(R10) ; SET STATUS
                                0DC4 1581 30$: :
                                0DC4 1582 BSBW UNBRK : RESTORE OPCODES
                                0DC7 1583 BBS #PSL$V_TBIT,SAVPSL-B(R11) : PROCEED IF BPT AND TBIT
                                0DCC 1584 MOVL R3,R5 : SAVE BPT NUMBER
                                0DCF 1585 BSBW CRLF : OUTPUT CR/LF PAIR
                                0DD2 1586 BSBW OUTDIGIT : OUTPUT BPT NUMBER
                                0DD5 1587 MOVAB MSG,R4 : MSG ADDRESS
                                0DD9 1588 BSBW OUTZSTRING : OUTPUT ASCIIZ
                                0DDC 1589 MOVL SAVPC-B(R11),R3 : OUTPUT PC
                                0DE0 1590 BSBW OUTLONG : OUTPUT HEX LONGWORD
                                0DE3 1591 MOVL BRKDISP[R5],R1 : GET ADDRESS TO DISPLAY
                                0DE9 1592 BEQL 40$ : NONE
                                0DEB 1593 MOVL R1,CURDOT-B(R11) : SET AS CURRENT DOT
                                0DEE 1594 BICW #12V_INSTR,(R10) : CLEAR INSTRUCTION DISPLAY MODE
                                0DF3 1595 BSBW LOCPROMPT : AND DISPLAY
                                0DF6 1596 40$: MOVL BRKCOM[R5],R1 : GET COMMAND STRING ADDRESS
                                0DFC 1597 BEQL OUTPC : NONE OUTPUT INSTRUCTION AT PC
                                0DFE 1598 MOVL R1,R9 : SET TO SCAN STORED COMMAND
                                0E01 1599 OUTPC: : OUTPUT PC INSTRUCTION & GET COMMANDS
                                0E01 1600 MOVL SAVPC-B(R11),CURDOT-B(R11) : SET ADDRESS
                                0E05 1601 IFNORD #4,2CURDOT-B(R11).GETCMD : SKIP DISPLAY IF NOT READABLE
```

```
6A 2000 8F AB OE0C 1602 BISW #12V INSTR,(R10) : SET TO INSTRUCTION DISPLAY MODE
      F8D6 30 OE11 1603 LOCPRMPT : PROMPT WITH ADDRESS/INSTRUCTION
      F734 CF 6C FA OE14 1604 GETCMD: : GET COMMANDS
      6E 10 OE19 1606 PROCEED: : PERFORM DEBUG COMMANDS
      20 6A 03 E5 OE19 1607 : SET BREAKPOINTS
      00 58 AB 04 E2 OE1B 1608 BBCC #V,TBIT,(R10),50$ : TEST AND CLR TRACE FLAG
      OE1F 1609 30$: BBSS #PSL$V,TBIT,SAVPSL-B(R11),40$ : SET TBIT
      OE24 1610 40$:
      OE24 1611 .IF DF,SW_PROCESS : FOR PROCESS VERSION
      54 BB 02 91 OE24 1612 CMPB #2,SAVPC-B(R11) : CHECK FOR REI OPCODE
      11 12 OE28 1613 BNEQ 45$ : NO, NOTHING SPECIAL
      50 58 AB 02 18 EF OE2A 1614 EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,SAVPSL-B(R11),R0 : GET NEW MODE
      50 00E4 8F A4 OE30 1615 MULW #CONTEXT$Z,R0 : SCALE BY PER MODE CONTEXT AREA SIZE
      5A F226 CF40 9E OE35 1616 MOVAB STATUS[R0],R10 : POINT TO NEW FLAGS
      OE3B 1617 .ENDC
      00 6A 05 E2 OE3B 1618 45$: BBSS #V,TBITOK,(R10),50$ : SET TBIT EXPECTED
      FF0B 30 OE3F 1619 50$: BSBW RESTORE : RESTORE EVERYTHING
      OE42 1620 .IF NDF,SW_PROCESS
      OE42 1621 REI
      OE42 1622 .IFF
      50 01 D0 OE42 1623 MOVL #1,R0 : AND RETURN
      04 OE45 1624 RET : FALSE IF PROCESS VERSION
      OE46 1625 .ENDC : RETURN TRUE
      OE46 1626
```



```
OE46 1628 .SBTTL TBIT EXCEPTION HANDLER
OE46 1629 :
OE46 1630 : HANDLER FOR TBIT EXCEPTION
OE46 1631 :
OE46 1632 .ALIGN LONG : LONGWORD ALIGNED
OE48 1633 .IF NDF,SW_PROCESS :
OE48 1634 XDELTBIT: : XDELTA TBIT HANDLER
OE48 1635 .IFF :
OE48 1636 XDELTBIT: :
OE48 1637 .ENDC :
06 6A FEAO 30 OE48 1638 BSBW SAVE : SAVE AND DISABLE
06 6A 05 E4 OE48 1639 BBSC #V TBITOK,(R10),XDELDBG : BR IF TBIT EXPECTED
06 6A FF06 30 OE4F 1640 BSBW RESTORR : RESTORE REGISTERS
OE52 1641 .IF NDF,SW_PROCESS :
OE52 1642 MOVZBL 6(SP),=(SP) : GET IPL FOR ENABLE
OE52 1643 ENBINT : ENABLE
OE52 1644 JMP EXESTBIT : OTHERWISE LET EXEC HANDLE
50 04 OE52 1645 .IFF : FALSE IF PROCESS VERSION
04 04 OE52 1646 CLRL R0 : RESIGNAL
OE54 1647 RET : UNEXPECTED TBIT EXCEPTION
OE55 1648 .ENDC :
58 AB 10 CA OE55 1649 XDELDBG: : COMMON WITH DEBUG EXCEPTION
06 10 OE55 1650 BICL #<10PSL$V_TBIT>,SAVPSL-B(R11) : CLEAR TBIT IN PSL
BA 6A 04 E4 OE59 1651 BSBB UNBRK : REPLACE OPCODES
A0 11 OE5B 1652 BBSC #V_ATBRK,(R10),PROCEED : CHECK FOR PROCEED
OE5F 1653 BRB OUTPC : DISPLAY INSTRUCTION AND GET COMMANDS
OE61 1654
```

```
.SBTTL UNBRK - RESTORE OPCODES FOR BREAKPOINTS
UNBRK
UNBRK:
50 51 09 D0 OE61 1656
F533 CF41 D0 OE61 1657
19 13 OE61 1658
OE61 1659
OE61 1660
OE61 1661 10$: MOVL #NBRK+NTMPBRK,R1
OE64 1662 MOVL BRKADR[R1],R0
OE6A 1663 BEQL 20$
OE6C 1664 .IF DF,SW PROCESS
OE6C 1665 PUSHF #^M<R0,R1,R2,R3,R4,R5>
OE6E 1666 MOVL R0,R4
OE71 1667 MOVL R0,R5
OE74 1668 BSBW SETWRT
OE77 1669 MOVQ (SP),R0
OE7A 1670 .ENDC
60 F544 CF41 90 OE7A 1671 MOVB BRKOP[R1],(R0)
OE80 1672 .IF DF,SW PROCESS
OE80 1673 BSBW REPROT
OE83 1674 POPR #^M<R0,R1,R2,R3,R4,R5>
OE85 1675 .ENDC
DC 51 F5 05 OE85 1676 20$: SOBGTR R1,10$
OE88 1677 RSB
OE89 1678

TOTAL PERM & TEMPORARY BREAKPOINTS
GET BREAKPOINT ADDRESS
SKIP IF NOT ENABLED
SAVE REGS FOR PROTECTION CHANGE
FORM INADR RANGE FOR SET PROTECTION
SET PAGE WRITABLE
RESTORE R0,R1
RESTORE OPCODE
RESTORE PROTECTION
RESTORE REGISTERS
DO THEM ALL
AND RETURN
```

```

                                OE89 1680 .SBTTL SETBRK - SET BREAK POINT INSTRUCTIONS
                                OE89 1681
                                OE89 1682
                                OE89 1683 SETBRK
                                OE89 1684 SETBRK: MOVL #NBRK+NTMPBRK,R1
50 51 09 D0 OE89 1685 10$: MOVL BRKADR[R1],R0
F50B CF41 D0 OE8C 1686 GEQL 20$
27 13 OE92 1687 MOVB (R0),BRKOP[R1]
F529 CF41 60 90 OE94 1688 BITB #<<10V_TBIT>!!<10V_ATBRK>>, (R10) ; CHECK FOR TRACE
6A 18 93 OE9A 1689 BEQL 15$
06 13 OE9D 1689 BEQL 15$
54 AB 50 D1 OE9F 1690 CMPL R0,SAVPC-B(R11)
16 13 OEA3 1691 BEQL 20$
OEA5 1692 15$:
OEA5 1693 .IF DF,SW PROCESS
OEA5 1694 PUSHR #*M<R0,R1,R2,R3,R4,R5>
54 50 D0 OEA7 1695 MOVL R0,R4
55 50 D0 OEAA 1696 MOVL R0,R5
0513 30 OEAD 1697 BSBW SETWRT
50 6E D0 OEB0 1698 MOVL (SP),R0
OEB3 1699 .ENDC
60 03 90 OEB3 1700 MOVB #3,(R0)
OEB6 1701 .IF DF,SW PROCESS
0541 30 OEB6 1702 BSBW REPROT
3F BA OEB9 1703 POPR #*M<R0,R1,R2,R3,R4,R5>
CE 51 F5 OEB8 1704 .ENDC
05 OEBB 1705 20$: SOBGTR R1,10$
OEBE 1706 RSB
OEBF 1707
                                : TOTAL PERMANENT & TEMPORARY BRKPOINTS
                                : GET ADDRESS
                                : SKIP IF NOT ENABLED
                                : SAVE OPCODE
                                : (R10) ; CHECK FOR TRACE
                                : NO TRACE, SET ANYWAY
                                : CHECK FOR AT BPT
                                : YES, DONT SET IT
                                :
                                : SAVE REGISTERS FOR PROTECTION CHANGE
                                : SET START ADDRESS OF RANGE
                                : AND END ADDRESS
                                : SET PAGE WRITABLE
                                : RESTORE BPT ADDRESS
                                :
                                : SET BREAKPOINT OPCODE
                                :
                                : RESTORE ORIGINAL PROTECTION VALUE
                                : AND REGISTERS
                                :
                                : DO THEM ALL
                                : AND RETURN
```

```

      OEBF 1709      .SBTTL GETBPTX - GET INDEX FOR BREAKPOINT
      OEBF 1710      GETBPTX
      OEBF 1711      GETBPTX:
      OEBF 1712      GETBPTX:
      OEBF 1713      GETBPTX:
      OEBF 1714      MOVL #NBRK,R3
      OEC2 1715 10$: CMPL SAVPC-B(R11),BRKADR[R3]
      OEC9 1716      BEQL 20$
      OECB 1717      SOBGTR R3,10$
      OECE 1718 20$: RSB

```

F4D3 CF43 53 08 DO  
 54 AB D1  
 03 13  
 F4 53 F5  
 05

: INIT LOOP  
 : IS THIS A BPT?  
 : YES  
 : NO, CONTINUE  
 : RETURN



			OECF	1720		.SBTTL	QUOTE - INPUT CHARACTER STRING	
			OECF	1721	:			
			OECF	1722	:			
			OECF	1723	:	QUOTE -	START CHARACTER STRING INPUT	
			OECF	1724	:			
55	6B	D0	OECF	1725	QUOTE:	MOVL	CURDOT-B(R11),R5	: POINT TO STRING BUFFER
	FAC6	30	OED2	1726	5\$:	BSBW	GETCHAR	: GET CHARACTER
58	27	91	OED5	1727		CMPB	#QUOT,R8	: CHECK FOR QUOTE
	05	13	OED8	1728		BEQL	10\$	: YES, END OF STRING
85	58	90	OEDA	1729		MOVB	R8,(R5)+	: INSERT IN BUFFER
	F3	11	OEDD	1730		BRB	5\$	: AND CONTINUE
6B	55	D0	OEDF	1731	10\$:	MOVL	R5,CURDOT-B(R11)	: SAVE NEW DOT
		05	OEE2	1732		RSB		: RETURN

```
DEPOSIT:
3F 6A 1F E0 OEE3 1734 .SBTTL DEPOSIT
54 6B D0 OEE3 1735 :
FO AB D5 OEE3 1736 : DEPOSIT DATA
44 12 OEE3 1737 :
OEE3 1738 DEPOSIT:
OEE3 1739 BBS #V_PREG,(R10),40$ BR IF PROCESSOR REGISTER
OEE7 1740 .IF DF,SW_PROCESS GET CURRENT DOT
OEE7 1741 MOVL CURDOT-B(R11),R4 CHECK FOR ARBITRARY PROCESS DEPOSIT
OEEA 1742 TSTL PID-B(R11) BR IF YES
OEEB 1743 BNEQ 50$
OEEF 1744 .ENDC
OEEF 1745 CASE CURTYPE-B(R11),TYPE=B,<- : SWITCH ON TYPE
OEEF 1746 10$,- BYTE
OEEF 1747 20$,- WORD
OEEF 1748 30$,- LONG
OEEF 1749 >
OEFA 1750 .IF NDF,SW_PROCESS
OEFA 1751 10$: MOVB F1-B(R11),@CURDOT-B(R11) STORE BYTE
OEFA 1752 RSB RETURN
OEFA 1753 20$: MOVW F1-B(R11),@CURDOT-B(R11) STORE WORD
OEFA 1754 RSB RETURN
OEFA 1755 30$: MOVL F1-B(R11),@CURDOT-B(R11) STORE LONG
OEFA 1756 RSB RETURN
OEFA 1757 40$: MTPR F1-B(R11),CURDOT-B(R11) SET VALUE IN PROCESSOR REGISTER
OEFA 1758 RSB
OEFA 1759 .IFF : FALSE IF PROCESS VERSION
OEFA 1760 10$: MOVL R4,R5 : BYTE DEPOSIT
OEFA 1761 BSBW SETWRT : START AND END ADDRESSES EQUAL
64 D8 AB 90 OF00 1762 MOVW F1-B(R11),(R4) : SET WRITABLE, OLD PROT TO R2
04F3 30 OF04 1763 BSBW REPROT : STORE BYTE
05 OF07 1764 RSB : RESTORE PROTECTION
OF08 1765
OF08 1766 20$: ADDL3 #1,R4,R5 : WORD DEPOSIT, FORM END ADDRESS
OF0C 1767 BSBW SETWRT : SET WRITABLE
64 D8 AB 80 OF0F 1768 MOVW F1-B(R11),(R4) : STORE WORD
04E4 30 OF13 1769 BSBW REPROT : RESTORE PROTECTION
05 OF16 1770 RSB
OF17 1771
OF17 1772 30$: ADDL3 #3,R4,R5 : LONGWORD DEPOSIT, FORM END ADDRESS
OF1B 1773 BSBW SETWRT : SET WRITABLE
64 D8 AB D0 OF1E 1774 MOVL F1-B(R11),(R4) : STORE LONG WORD
04D5 30 OF22 1775 BSBW REPROT : RESTORE PROTECTION
05 OF25 1776 RSB
OF26 1777
OF26 1778
OF26 1779 40$: $CMKRNLS B*DEPPREG,(AP) : PROCESSOR REGISTER
05 OF32 1780 RSB DEPOSIT IN PROCESSOR REGISTER
OF33 1781 50$: DEPOSIT IN ARBITRARY PROCESS
OF33 1782 CASE CURTYPE-B(R11),TYPE=B,<- : SWITCH ON TYPE
OF33 1783 60$,- BYTE
OF33 1784 70$,- WORD
OF33 1785 80$> LONGWORD
OF3E 1786
OF3F 1787 RSB
14EE'CF 9F OF3F 1788 60$: PUSHAB W*DPBYTE : SET ADDRESS OF BYTE ROUTINE
0A 11 OF43 1789 BRB 90$
1550'CF 9F OF45 1790 70$: PUSHAB W*DPWORD : SET ADDRESS OF WORD ROUTINE
```

```
04 11 0F49 1791 BRB 90$ ;  
15B1'CF 9F 0F4B 1792 80$: PUSHAB W^DPLONG ; SET ADDRESS OF LONG ROUTINE  
FO AB DD 0F4F 1793 90$: PUSHL PID-B(R11) ; SET PID OF TARGET  
6B DD 0F52 1794 PUSHL CURDOT-B(R11) ; ADDRESS FOR STORE  
DB AB DD 0F54 1795 PUSHL F1-B(R11) ; VALUE TO STORE  
04 DD 0F57 1796 PUSHL #4 ; ARGUMENT COUNT  
50 5E D0 0F59 1797 MOVL SP,R0 ; POINTER TO ARGUMENT LIST  
EC AB D5 0F5C 1798 TSTL MFYFLG-B(R11) ; CHECK FOR STORE ENABLED  
OD 13 0F5F 1799 BEQL 100$ ; BR IF NOT  
SE 14 C0 0F61 1800 $CMKRNL-S W^QGET,(R0) ; CALL TO QUEUE REQUEST  
05 0F6E 1801 100$: ADDL #20,SP ; CLEAN STACK  
0F71 1802 RSB ; AND RETURN  
0F72 1803  
0000 0F72 1804 DEPPREG: .WORD 0 ; DEPOSIT INTO PROCESSOR REGISTER  
6D 0F81'CF 9E 0F74 1805 MOVAB W^PREXC,(FP) ; SET EXCEPTION HANDLER  
6B DB AB DA 0F79 1806 MTPR F1-B(R11),CURDOT-B(R11) ; PLACE FIELD VALUE IN REG  
50 01 D0 0F7D 1807 MOVL #1,R0 ; RETURN SUCESS  
04 0F80 1808 RET ;  
0F81 1809  
0000 0F81 1810 PREXC: .WORD 0 ; PROCESSOR REGISTER EXCEPTION HANDLER  
51 08 AC 04 C1 0F83 1811 ADDL3 #4,8(AP),R1 ; POINT TO EXCEPTION FP  
OC AD 61 D0 0F88 1812 MOVL (R1),12(FP) ; SET AS RETURN FP  
10 AD 91'AF 9E 0F8C 1813 MOVAB B^10$,16(FP) ; SET RETURN ADDRESS  
50 01 3C 0F91 1814 10$: MOVZWL #1,R0 ; SET NORMAL STATUS  
04 0F94 1815 RET ; AND RETURN  
0F95 1816  
0F95 1817 .ENDC
```

```

                                .SBTTL EXECUTE - PERFORM COMMAND STRING
                                EXECUTE
                                EXECUTE:
09 6A 08 E1 0F95 1819                                BBC      #V F1,(R10),10$
59      DB AB D0 0F95 1820                                MOVL     F1=B(R11),R9
      03 12 0F95 1821                                BNEQ     10$
      F5BB 31 0F95 1822                                BRW      SUPERST
      05 0FA2 1823 10$:                                RSB
      0FA3 1824                                ;
                                ; EXIT IF NO ADDRESS
                                ; SET CHAR STRING
                                ; NOT NULL
                                ; SUPER RESET
                                ; RETURN

```



DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
P - PROCESSOR REGISTER PREFIX

F 2

15-SEP-1984 23:38:31 VAX/VMS Macro V04-00  
5-SEP-1984 00:08:35 [DELTA.SRC]XDELTA.MAR;1

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```

      OFA3 1831      .SBTTL P - PROCESSOR REGISTER PREFIX
      OFA3 1832      SET PROCESSOR REGISTER MODE
      OFA3 1833      :
      OFA3 1834      :
      OFA3 1835      :
      OFA3 1836      PREG:      : PROCESSOR REGISTER MODE
00 6A  OF  E2  OFA3 1836      BBSS #V_PMODE,(R10),10$      : SET PROCESSOR REG FLAG
      OS  OFA7 1837 10$:      RSB      : RETURN
```

```
73 72 65 56 20 41 54 4C 45 44 0A 0D 00 0A 0D 32 2E 32 58 20 6E 6F 69
00 0A 0D 32 2E 32 58 20 6E 6F 69

F499 CF F52D CF 9E OFD3 1850
F47C CF F483 CF 9E OFDA 1851
F497 CF F49E CF 9E OFE1 1852
F4A1 CF F4A8 CF 9E OFE8 1853
F4E4 CF 0000129C'EF 9E OFEF 1854
F4E5 CF F4EC CF 9E OFFB 1855
OB'AF 6C FA OFFF 1856
04 1003 1857
1004 1858
5C 04 AC D0 1004 1859
01C4' 31 1008 1860
100B 1861
100B 1862
6D 1263'CF 9E 100D 1864
1012 1865
16 50 E8 101F 1866
1022 1867
06 50 E8 102F 1868
24 50 D1 1032 1869
01 13 1035 1870
04 1037 1871
1038 1872
1038 1873
1038 1874
1049 1875
1049 1876
1049 1877
105A 1878
3C BB 1065 1879
54 EF95 CF 9E 1067 1880
55 15C8'CF 9E 106C 1881
034F 30 1071 1882
3C BA 1074 1883
1076 1884
1076 1885
1076 1886
50 50 E9 108F 1887
00000629 8F 50 D1 1092 1888
47 13 1099 1889
109B 1890
109B 1891
109B 1892
00000629 8F 50 D1 1084 1893
DE 12 108B 1894
```

```
.SBTTL PROCESS DEBUGGER INITIALIZATION
.SALUTE: .IF DF,SW PROCESS
.SALUTE: .ASCIZ <CR><LF>/DELTA Version x2.2/<CR><LF> ;

TEST: ; START ADDRESS OF IMAGE ENTRY
XDT$START:: ; GLOBAL START ADDRESS FOR CLI DEBUG
.WORD 0
DELTA_START: ; START ADDRESS FOR DEBUGGER ENTRY
SWAKE S ; NULL WAKE AND
SHIBER S ; HIBERNATE TO GET SYNCHRONIZED
MOVAB TERMASK,TERMASKADR ; RELOCATE TERMINATOR MASK DESCR
MOVAB TTNAME+8,TTNAME+4 ; RELOCATE DESCRIPTOR
MOVAB DBGINPUT+8,DBGINPUT+4
MOVAB TRNINPUT+8,TRNINPUT+4
MOVAB EXIHANDLE,EXIHADR ;
MOVAB EXITCODE,EXCODA ; RELOCATE EXIT HANDLER ARGS
CALLG (AP),B*INITCALL ; GENERATE CALL FRAME
RET ;

NOBRK: MOVL 4(AP),AP ; GET EXCEPTION ARGUMENT LIST
BRW EXCEPT+2 ; AND GOTO EXCEPTION HANDLER

INITCALL:
.WORD 0 ; ENTRY MASK
MOVAB W*CATCHALL,(FP) ; SET CATCHALL EXCEPTION HANDLER
$CMKRNLS W*SETKEXC,(AP) ; SET EXCEPTION VECTORS FROM KERNEL MODE
BLBS RO,18 ; SUCCESS WITH KERNEL, IF NOT, TRY EXEC
$CMEXECS W*SETEEXC,(AP) ; SET EXEC & SUPV EXCEPTION VECTORS
BLBS RO,18 ; BRANCH IF SUCCESS
CMPL RO,#SS$_NOPRIV ; CHECK FOR LACK OF PRIVILEGE
BEQL 18 ; WHICH IS THE ONLY ACCEPTABLE ERROR
RET ; OTHERWISE GET OUT
18: $SETEXVS ADDRES=W*EXCEPT ;
ACMODE=#PSL$_USER ;
VECTOR=#0 ; SET PRIMARY FOR USER
$SETEXVS ADDRES=W*CATCHALL ; SET LAST CHANCE HANDLER
ACMODE=#PSL$_USER ; FOR USER MODE
VECTOR=#2 ; SPECIFY LAST CHANCE HANDLER
$DCLEXHS EXITBLK ; DECLARE USER MODE EXIT HANDLER
PUSHR #M<R2,R3,R4,R5>
MOVAB W*DELBASE,R4 ; Set page protection on entire
MOVAB W*DELEND,R5 ; DELTA image to user writeable.
BSBW SETWRT
POPR #M<R2,R3,R4,R5>
$TRNLOGS LOGNAM=DBGINPUT, ; FIRST DEFAULT INPUT
RSLLEN=TRNINPUT,
RSLBUF=TRNINPUT
BLBC RO,98 ; ON ERROR, USE TT
CMPL RO,#SS$_NOTRAN ; USE TT IF NO TRANSLATION
BEQL 98 ; TRANSLATE ALL THE WAY
$TRNLOGS LOGNAM=TRNINPUT,
RSLLEN=TRNINPUT,
RSLBUF=TRNINPUT
CMPL RO,#SS$_NOTRAN ; TRY ANOTHER LEVEL
BNEQ 38
```

```
1B F3CF DF 91 10BD 1895 CMPB @TRNINPUT+4,#^X1B ; CHECK FOR PROCESS PERMANENT
F3C3 CF 0A 12 10C2 1896 BNEQ 58
F3C2 CF 04 C2 10C4 1897 SUBL #4,TRNINPUT ; REMOVE THE ESCAPE HEADER
F3C2 CF 04 C0 10C9 1898 ADDL #4,TRNINPUT+4
15 50 E8 10CE 1899 58: $ASSIGN_S TRNINPUT,TTCHAN ; DO THE ASSIGN
01 50 E8 10DF 1900 98: BLBS -R0,108 ; TRY IT ON ERROR
04 10F3 1901 98: $ASSIGN_S TTNAME,TTCHAN ; ASSIGN DEVICE
04 10F6 1902 BLBS -R0,108 ; CONTINUE IF SUCCESS
54 FEAD CF 9E 10F7 1903 108: RET ; ELSE EXIT WITH ERROR CODE IN R0
F7E6 30 10FC 1905 MOVAB SALUTE,R4 ; SET ADDRESS OF SALUTATION
03 18 AC 10 10FF 1906 BBC #CLISV_DBGEXCP,24(AP),158 ; BR IF LATER INVOCATION
FEFD 31 1104 1907 BRW NOBRK ; VIA $DEBUG COMMAND
OC'AF 6C FA 1107 1908 158: CALLG (AP),B^208 ; CREATE TOP CALL FRAME
04 110B 1909 RET
04 AC 04 0000 110C 1910 208: .WORD 0 ; NULL ENTRY MASK
7E 04 BC 02 1112 1912 ADDL #4,4(AP) ; ADVANCE STARTING ADDRESS POINTER
7E 046C 8F 3C 1114 1913 MOVPSL -(SP) ; SAVE PSL
50 03 DD 1119 1914 ADDL3 #2,24(AP),-(SP) ; FETCH CURRENT STARTING ADDRESS
7E 50 5E DO 111E 1915 MOVZWL #$$$_DEBUG,-(SP) ; SET EXCEPTION CODE
50 50 7D 1120 1916 PUSHL #3 ; SIGNAL ARG COUNT
7E 50 DD 1123 1917 MOVL SP,R0 ; SAVE POINTER
50 5D DD 1126 1918 MOVQ R0,-(SP) ; SAVE PHONY R0,R1
50 04 DD 1128 1919 PUSHL #0 ; DEPTH
50 5E DD 112A 1920 PUSHL FP ; FP
50 50 DD 112C 1921 PUSHL #4 ; ARG COUNT
50 5E DD 112E 1922 PUSHL SP ; POINTER TO MECH
11CD'CF 02 FB 1130 1923 PUSHL RO ; POINTER TO SIGNAL
5E 0C CO 1135 1924 CALLS #2,W^EXCEPT ; SIGNAL PHONY EXCEPTION
50 8E 7D 1138 1925 ADDL #12,SP ; CLEAN BACK TO R0,R1
5E 08 CO 113B 1926 MOVQ (SP)+,R0 ; RESTORE R0,R1
02 113E 1927 ADDL #8,SP ; CLEAN BACK TO PC,PSL
113F 1928 REI ; RETURN TO TARGET PROGRAM
113F 1929
113F 1930
113F 1931
113F 1932
113F 1933
113F 1934
113F 1935
113F 1936
113F 1937
113F 1938
113F 1939
113F 1940
113F 1941
113F 1942
113F 1943
113F 1944
113F 1945
113F 1946
113F 1947
113F 1948
113F 1949
113F 1950
113F 1951

12FA'CF 0000 1141 1933 SETKEXC: .ENABLE LOCAL_BLOCK
1361'CF 01 FB 1145 1934 PUSHAB .WORD 0 ; ENTRY MASK FOR CMKRNL PRIVILEGE
7F 50 E9 114A 1935 CALLS W^CLREXV_KERNEL ; SET TO USE KERNEL RUNDOWN HANDLER
114D 1936 BLBC RO,208 ; SET UP APPROPRIATE RUNDOWN HANDLER
114D 1937 $SETEXV_S ADDRES=B^EXCEPT,- ; BRANCH IF CAN'T SET UP HANDLER
114D 1938 PRVHND=KCOND_PRIMARY,- ; SET KERNEL
114D 1939 ACMODE=#PSLSC_KERNEL,- ; PRIMARY VECTOR
114D 1940 VECTOR=#0 ; SET KERNEL
114D 1941 $SETEXV_S ADDRES=W^CATCHALL,- ; PRIMARY VECTOR
114D 1942 PRVHND=KCOND_LASTCHANC,- ; SET KERNEL MODE LAST CHANCE HANDLE
114D 1943 ACMODE=#PSLSC_KERNEL,- ; SPECIFY LAST CHANCE VECTOR
114D 1944 VECTOR=#2 ; SKIP ALTERNATE ENTRY MASK
114D 1945
114D 1946
114D 1947
114D 1948
114D 1949
114D 1950
114D 1951

0E 11 1172 1944 BRB 108
0000 1174 1945
131C'CF 0000 1176 1947 SETEEXC: .WORD 0 ; ENTRY MASK FOR CMEXEC PRIVILEGE
1361'CF 01 FB 117A 1948 PUSHAB W^CLREXV_EXEC ; SET TO USE EXEC RUNDOWN HANDLER
4A 50 E9 117F 1949 CALLS #1,W^SETRUNDWN ; SET UP APPROPRIATE RUNDOWN HANDLER
1182 1950 BLBC RO,208 ; BRANCH IF CAN'T SET UP HANDLER
1182 1951 $SETEXV_S ADDRES=B^EXCEPT,- ;
PRVHND=ECOND_PRIMARY,- ;
```

```
1182 1952 ACMODE=#PSLSC_EXEC,- ; SET EXEC MODE EXCEPTION HANDLER
1182 1953 VECTOR=#0 ; PRIMARY VECTOR
1194 1954 $SETEXV_S ADDRESS=W^CATCHALL,-
1194 1955 PRVHND=ECOND_LASTCHANC,-
1194 1956 ACMODE=#PSLSC_EXEC,- ; SET EXEC MODE LAST CHANCE HANDLER
1194 1957 VECTOR=#2 ; SPECIFY LAST CHANCE VECTOR
-----
11A7 1958 $SETEXV_S ADDRESS=B^EXCEPT,-
11A7 1959 PRVHND=SCOND_PRIMARY,- ; SET SUPERVISOR MODE EXCEPTION HAND
11A7 1960 ACMODE=#PSLSC_SUPER,- ; PRIMARY VECTOR
11A7 1961 VECTOR=#0
11B9 1962 $SETEXV_S ADDRESS=W^CATCHALL,-
11B9 1963 PRVHND=SCOND_LASTCHANC,-
11B9 1964 ACMODE=#PSLSC_SUPER,- ; SET SUPERVISOR LAST CHANCE HANDLER
11B9 1965 VECTOR=#2 ; SPECIFY LAST CHANCE VECTOR
04 11B9 1966 20$: RET
11CD 1967 .DISABLE LOCAL_BLOCK
11CD 1968
11CD 1969
11CD 1970
0000 11CD 1971 EXCEPT: .WORD 0 ; EXCEPTION HANDLER ENTRY MASK
11CF 1972 $SETEXV_S ADDRESS=B^EXCEPT,-
11CF 1973 ACMODE=#PSLSC_USER,- ; RE-ESTABLISH USER PRIMARY VECTOR
11CF 1974 VECTOR=#0 ; GET POINTER TO SIGNAL
50 04 AC 04 C1 11DF 1975 ADDL3 #4,4(AP),R0 ; GET CURRENT PSL
51 51 02 18 DC 11E4 1976 MOVPSL R1 ;
43 F2E4 CF 51 E2 11E6 1977 EXTZV #PSLSV_CURMOD,#PSLSS_CURMOD,R1,R1 ;
60 00000464 8F D1 11EB 1978 BBSS R1,DBGACTIVE,40$ ; BR IF ALREADY ACTIVE
03 12 11F8 1979 CMPL #SS$_TBIT,(R0) ; IS IT TBIT?
FC4B 31 11FA 1980 BNEQ 10$ ; NO
60 00000414 8F D1 11FD 1981 BRW XDELTBIT ; YES, A TBIT
03 12 1204 1982 CMPL #SS$_BREAK,(R0) ; IS IT BREAKPOINT?
FB97 31 1206 1983 BNEQ 20$ ; NO
1209 1984 15$: BRW XDELBPT ; YES, A BREAKPOINT
1209 1985 20$: CMPL #SS$_UNWINDING,(R0) ; SOME OTHER EXCEPTION
1210 1986 BEQL 60$ ; IS IT UNWINDING
1212 1987 CMPL #SS$_COMPAT,(R0)+ ; YES
1219 1988 BNEQ 30$ ; IS IT COMPATIBILITY MODE EXCEPT?
60 01 D1 121B 1989 CMPL #1,(R0) ; NO
E6 13 121E 1990 BEQL 15$ ; IS IT COMPATIBILITY BPT?
60 07 D1 1220 1991 CMPL #7,(R0) ; YES
D5 13 1223 1992 BEQL 5$ ; IS IT COMPATIBILITY TBIT?
70 0000046C 8F D1 1225 1993 CMPL #SS$_DEBUG,-(R0) ; YES
06 12 122C 1994 BNEQ 40$ ; IS IT DEBUG EXCEPTION?
FABA 30 122E 1995 BSBW SAVE ; NO
FC21 31 1231 1996 BRW XDELDDBG ; SAVE EVERYTHING
00 F29B CF 51 E5 1234 1997 BBCC R1,DBGACTIVE,50$ ; AND TREAT AS FUNNY BPT
50 D4 123A 1998 CLRL R0 ; UNEXPECTED EXCEPTION
04 123C 1999 RET ; CLEAR DEBUG ACTIVE
50 01 D0 123D 2000 RET ; RETURN FALSE FOR RESIGNAL
04 1240 2001 60$: MOVL #1,R0 ;
04 1240 2002 RET ; IGNORE AND RESIGNAL
04 1240 2003 RET ;
```



```
1241 2005 .SBTTL HANDLER FOR DEBUG EXCEPTIONS
1241 2006
1241 2007 DBGEXCEP:
1241 2008 .WORD 0
1243 2009 ADDL3 #4,8(AP),R1
1248 2010 MOVL FP,R0
1248 2011 10$: CMPL 12(R0),(R1)
124F 2012 BEQL 20$
1251 2013 MOVAB B^30$,16(R0)
1256 2014 MOVL 12(R0),R0
125A 2015 BRB 10$
125C 2016 20$: MOVAB XDELACV,16(R0)
1262 2017 30$: RET
1263 2018
1263 2019 CATCHALL:
1263 2020 .WORD 0
1265 2021 MOVPSL R1
1267 2022 EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1
126C 2023 BBCS R1,DBGACTIVE,10$
1272 2024 CLRL R0
1274 2025 RET
1275 2026 10$: BSBW SAVE
1278 2027 ADDL3 #4,4(AP),R0
127D 2028 MOVL (R0),R3
1280 2029 BSBW CRLF
1283 2030 BSBW OUTLONG
1286 2031 MOVAB B^EXCMMSG,R4
128A 2032 BSBW OUTZSTRING
128D 2033 BRW XDELDBG
1290 2034 EXCMMSG: .ASCIZ / EXCEPTION /
129C 2035
129C 2036 EXIHANDLE:
129C 2037 .WORD 0
129E 2038 BITB #15,DBGACTIVE
12A3 2039 BEQL 10$
12A5 2040 RET
12A6 2041 10$:
12A6 2042 MOVPSL -(SP)
12A8 2043 PUSHL 16(FP)
12AB 2044 PUSHL B4(AP)
12AE 2045 PUSHL #3
12B0 2046 PUSHR #^M<R0,R1>
12B2 2047 MOVQ AP,-(SP)
12B5 2048 PUSHL #4
12B7 2049 PUSHL SP
12B9 2050 PUSHAL 24(SP)
12BC 2051 PUSHL #2
12BE 2052 MOVL SP,AP
12C1 2053 BSBW SAVE
12C4 2054 MOVAB B^EXIMSG,R4
12C8 2055 BSBW OUTZSTRING
12CB 2056 MOVL SAVAP-B(R11),R3
12CF 2057 MOVL 4(R3),R3
12D3 2058 BSBW OUTLONG
12D6 2059 $DCLEXH S EXITBLK
12E1 2060 MOVPSL R1
12E3 2061 EXTZV #PSL$V_CURMOD,#PSL$S_CURMOD,R1,R1 ; GET CURRENT MODE

0000 1241 2005
51 08 AC 04 C1 1243 2009
50 5D D0 1248 2010
61 0C A0 D1 1248 2011
0B 13 124F 2012
10 A0 62 AF 9E 1251 2013
50 0C A0 D0 1256 2014
EF 11 125A 2015
10 A0 FA88 CF 9E 125C 2016
04 1262 2017
0000 1263 2018
51 51 02 18 DC 1265 2021
03 F263 CF 51 E3 1267 2022
50 50 D4 126C 2023
04 1272 2024
FA73 30 1274 2025
50 04 AC 04 C1 1275 2026
53 60 D0 1278 2027
F70E 30 127D 2028
F642 30 1280 2029
54 90 AF 9E 1283 2030
F658 30 1286 2031
FBC5 31 128A 2032
00 20 4E 4F 49 54 50 45 43 58 45 20 128D 2033
0000 1290 2034
F231 CF 0F 93 129C 2035
01 13 129C 2036
04 129E 2037
7E DC 12A3 2038
10 AD DD 12A5 2039
04 BC DD 12A6 2040
03 DD 12A8 2041
03 BB 12AA 2042
7E 5C 7D 12AC 2043
04 DD 12AD 2044
5E DD 12AE 2045
18 AE DF 12B0 2046
02 DD 12B2 2047
5C 5E DD 12B5 2048
FA27 30 12B7 2049
54 F1 AF 9E 12B9 2050
F61A 30 12BC 2051
53 48 AB D0 12BE 2052
53 04 A3 D0 12C1 2053
F5F2 30 12C4 2054
51 51 02 18 DC 12C8 2055
EF 12CB 2056
12CF 2057
12D3 2058
12D6 2059
12E1 2060
12E3 2061
```



```
00 F1E7 CF 51 E2 12E8 2062 BBSS R1,DBGACTIVE,20$ ; SET DELTA ACTIVE FOR MODE
FB64 31 12EE 2063 20$: BRW XDÉDBG ;
00 20 54 49 58 45 20 0A 0D 12F1 2064 EXIMSG: .ASCIZ <CR><LF>/ EXIT / ;
12FA 2065
12FA 2066
12FA 2067
12FA 2068 .ENABLE LOCAL_BLOCK
12FA 2069
12FA 2070 : RESET INNER MODE EXCEPTION VECTORS. THIS CODE IS CALLED AS A
12FA 2071 : PRIVILEGED IMAGE RUNDOWN HANDLER TO ENSURE ITS EXECUTION.
12FA 2072
12FA 2073 : THIS ENTRY POINT IS USED IF THE PROCESS HAS CMKRNL PRIVILEGE
12FA 2074
12FA 2075 CLREXV_KERNEL: ; CLEAR EXCEPTION VECTORS
12FA 2076
12FA 2077 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR KERNEL MODE
12FA 2078
12FA 2079 $SETEXV_S ADDRESS=@KCOND_PRIMARY,- ;
12FA 2080 ACMODE=#PSL$C_KERNEL,- ;
12FA 2081 VECTOR=#0 ; PRIMARY VECTOR
130B 2082 $SETEXV_S ADDRESS=@KCOND_LASTCHANC,- ;
130B 2083 ACMODE=#PSL$C_KERNEL,- ;
130B 2084 VECTOR=#2 ; LAST CHANCE VECTOR
131C 2085
131C 2086 : THIS ENTRY POINT IS USED IF THE PROCESS HAS CMEXEC PRIVILEGE
131C 2087
131C 2088 CLREXV_EXEC: ; CLEAR EXCEPTION VECTORS
131C 2089
131C 2090 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR EXECUTIVE MODE
131C 2091
131C 2092 $SETEXV_S ADDRESS=@ECOND_PRIMARY,- ;
131C 2093 ACMODE=#PSL$C_EXEC,- ;
131C 2094 VECTOR=#0 ; PRIMARY VECTOR
132D 2095 $SETEXV_S ADDRESS=@ECOND_LASTCHANC,- ;
132D 2096 ACMODE=#PSL$C_EXEC,- ;
132D 2097 VECTOR=#2 ; LAST CHANCE VECTOR
133E 2098
133E 2099 : RESET PRIMARY AND LAST CHANCE EXCEPTION VECTORS FOR SUPERVISOR MODE
133E 2100
133E 2101 $SETEXV_S ADDRESS=@SCOND_PRIMARY,- ;
133E 2102 ACMODE=#PSL$C_SUPER,- ;
133E 2103 VECTOR=#0 ; PRIMARY VECTOR
134F 2104 $SETEXV_S ADDRESS=@SCOND_LASTCHANC,- ;
134F 2105 ACMODE=#PSL$C_SUPER,- ;
134F 2106 VECTOR=#2 ; LAST CHANCE VECTOR
05 1360 2107 RSB ;
1361 2108
1361 2109 .DISABLE LOCAL_BLOCK
```

```
1361 2111 .SBTTL SETRUNDWN - SET UP RUNDOWN HANDLER
1361 2112
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1361 2167

++
FUNCTIONAL DESCRIPTION:
    This routine inserts the specified routine entry point into
    the process' rundown vector. CMKRNL privilege of running in
    kernel or exec mode is required.

CALLING SEQUENCE:
    CALLx SETRUNDWN

INPUT PARAMETERS:
    4(AP): address of rundown handler routine

IMPLICIT INPUTS:
    NONE

OUTPUT PARAMETERS:
    NONE

IMPLICIT OUTPUTS:
    NONE

COMPLETION CODES:
    $$$_NORMAL if successfully completed
    $$$_NOPRIV if not privileged

SIDE EFFECTS:
    NONE

--

.WEAK CTL$GL_USRUNDWN

SETRUNDWN:
    .WORD *M<>
    MOVPSL R0
    CMPZV #PSL$V_PRIVMOD, #PSL$S_PRIVMOD, R0, #PSL$C_USER
    BLSSU $S
    PUSHL AP
    PUSHAB 10$
    CALLS #2, G^SYSSCMKRNL
    RET

5$:
    MOVL #$$$_NORMAL, R0
    RET

: The following code executes in kernel mode and actually inserts the
: entry point into the rundown vector.

10$:
    .WORD *M<>
    SETIPL #IPL$ ASTDEL
    MOVAB G^CTL$GL_USRUNDWN, R1
```

```
03 50 02 50 0000
    16 DC
    10 ED
    5C 1F
    00001380'EF DD
    00000000'GF 9F
    02 FB
    04
    50 01 D0
    04
    0000
    51 00000000'GF 9E
```

```
; get PSL of caller
; branch if DELTA started up in inner mode
; build $CMKRNL arg list on stack
; address of kernel mode routine
; call actual routine in kernel mode
; DELTA started up in an inner mode
; do not set up a rundown handler
```

```
; save no registers
; lock out AST's while we modify the vector
; Get rundown vector pointer address.
```

51	61	2E	13	138C	2168	BEQL	19\$	:	Branch to NOP routine if no present.
50	00002034	04	C3	138E	2169	SUBL3	#4,(R1),R1	:	Get address of rundown vector.
000000F9	8F	8F	D0	1392	2170	MOVL	#SS\$ _VEC(FULL,R0	:	
		61	D1	1399	2171	CMPL	(R1),#<256-7>	:	check if another vector will fit
		1D	1E	13A0	2172	BGEQU	20\$	:	branch if not
50	51	61	C1	13A2	2173	ADDL3	(R1),R1,R0	:	point to free vector
80	9F16	8F	B0	13A6	2174	MOVW	#*X9F16,(R0)+	:	insert JSB @#...
80	04	AC	D0	13AB	2175	MOVL	4(AP),(R0)+	:	insert routine address
	80	05	90	13AF	2176	MOVB	#*X05,(R0)+	:	insert final RSB
	61	06	C0	13B2	2177	ADDL	#6,(R1)	:	update end pointer
00000004	GF	06	A0	13B5	2178	ADDW	#6,G* IAC\$AW_VECSET+4	:	adjust image activation end pointer
	50	01	D0	13BC	2179	MOVL	#SS\$ _NORMAL,R0	:	indicate success
				13BF	2180	SETIPL	#0	:	enable ASI's again
			04	13C2	2181	RET		:	

```
13C3 2183 .SBTTL SETWRT - SET PAGES WRITABLE
13C4 2184
13C5 2185 MAKE SPECIFIED RANGE OF PAGES WRITABLE
13C6 2186
13C7 2187 R4 = STARTING ADDRESS
13C8 2188 R5 = ENDING ADDRESS
13C9 2189
13CA 2190 R0-R2 DESTROYED
13CB 2191
13CC 2192
13CD 2193 SETWRT:
13CE 2194 MOVQ R4, -(SP)
13CF 2195 MOVAL -(SP), R2
13D0 2196 $CMKRNLS B*SETPRTK, (R2)
13D1 2197 BLBS R0, 10B
13D2 2198 CALLG (R2), B*SETPRTK
13D3 2199 10B: POPR #*M<R2>
13D4 2200 ADDL #B, SP
13D5 2201 RSB
13D6 2202
13D7 2203 SETPRTK: WORD 0
13D8 2204 $SETPRT_S
13D9 2205 INADR=4(AP), -
13DA 2206 PROT=#PRTSC_UW, -
13DB 2207 ACMODE=#0, -
13DC 2208 PRVPRT=(AP)
13DD 2209
13DE 2210 MOVL #1, R0
13DF 2211 RET
13E0 2212 REPROT:
13E1 2213 RSB
13E2 2214
13E3 2215
13E4 2216
13E5 2217
13E6 2218
13E7 2219
13E8 2220
13E9 2221
13EA 2222
13EB 2223
13EC 2224
13ED 2225
13EE 2226
13EF 2227
13F0 2228
13F1 2229
13F2 2230
13F3 2231
13F4 2232
13F5 2233
13F6 2234
13F7 2235
13F8 2236
13F9 2237
13FA 2238
13FB 2239
13FC 2240
13FD 2241
13FE 2242
13FF 2243
1400 2244
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1402 2246
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13FB 2214 .SBTTL FETCHP - FETCH DATA FROM ANOTHER PROCESS
13FB 2215
13FB 2216 FETCHP: CASE CURTYPE-B(R11),TYPE=B,<-
13FB 2217 10$,- : 0 => BYTE
13FB 2218 20$,- : 1 => WORD
13FB 2219 30$> : 2 => LONG
: UNKNOWN
1406 2220 RSB : SET FOR BYTE FETCH
1407 2221 10$: PUSHAB W^FPBYTE
140B 2222 BRB 40$
140D 2223 20$: PUSHAB W^FPWORD
1411 2224 BRB 40$ : SET FOR WORD FETCH
1413 2225 30$: PUSHAB W^FPLONG
1417 2226 40$: PUSHL PID-B(R11) : SET FOR LONGWORD FETCH
141A 2227 PUSHAB QUAN-B(R11) : PID OF TARGET PROCESS
141D 2228 PUSHL CURDOT-B(R11) : SET ADDRESS TO RETURN VALUE
141F 2229 PUSHL #4 : AND ADDRESS OF VALUE
1421 2230 MOVL SP,R0 : ARGUMENT COUNT
1424 2231 $CMKRNLS W^QGET,(R0) : SAVE POINTER TO ARG LIST
1431 2232 BLBC RO,50$ : Q AST FOR DATA FETCH
1434 2233 $HIBER_S : BR IF FAILED
143B 2234 50$: ADDL #20,SP : WAIT FOR DATA TO RETURN
143E 2235 RSB : CLEAN STACK
: AND RETURN DATA
```



```
143F 2237 .SBTTL QGET - QUEUE AST TO GET DATA FROM ANOTHER PROCESS
143F 2238
143F 2239 :
143F 2240 :
143F 2241 :
143F 2242 :
143F 2243 :
143F 2244 :
143F 2245 :
143F 2246 :
143F 2247 :
143F 2248 :
143F 2249 :
00000010 143F 2250 FP_ORIGPID=ACBSL_AST
00000014 143F 2251 FP_ADDR=ACBSL_ASTPRM
00000014 143F 2252 FP_VALUE=ACBSL_ASTPRM
0000001C 143F 2253 FP_RETLOC=ACBSL_KAST+4
143F 2254
003C 143F 2255 QGET: .WORD *M<R2,R3,R4,R5> : ENTRY MASK
50 08E8 8F 3C 1441 2256 MOVZWL #SSS_NONEXPR,R0 : ASSUME BAD PIX
51 00000000'GF 9E 1446 2257 MOVAB G*EXESALLOCBUF,R1 : WERE WE LINKED WITH SYS.STB SYMBOLS?
53 13 144D 2258 BEQL 10$ : IF NOT, RETURN WITH ERROR
00000000'9F 0C AC B1 144F 2259 CMPW 12(AP),@NSCH$GL_MAXPIX : CHECK PIX FOR LEGAL PROCESS
49 1A 1457 2260 BGTRU 10$ : BR IF NOT
```

```

      51 10 BC 3C 1459 2262 MOVZWL @16(AP),R1 : GET SIZE OF CODE SEGMENT
      51 00C4 C1 9E 145D 2263 MOVAB IRPSC_LENGTH(R1),R1 : ADD SIZE OF PACKET DATA
      00000000'9F 16 1462 2264 JSB @#EXES$ALLOCBUF : ALLOCATE BUFFER TO CONTAIN CODE
      37 50 E9 1468 2265 BLBC R0,10$ : BRANCH IF NONE
      55 52 D0 146B 2266 MOVL R2,R5 : SAVE ADDRESS OF PACKET
      10 A5 60 A4 D0 146E 2267 PCBSL_PID(R4),FP_ORIGPID(R5) : SET PID FOR RETURN
      08 A5 80 BF 90 1473 2268 MOVB #^X80,ACBSB_RMOD(R5) : SET FOR SPECIAL KERNEL AST
      18 A5 20 A5 9E 1478 2269 MOVAB ACBSL_KAST+8(R5),ACBSL_KAST(R5) : SET ADDRESS FOR AST
      14 A5 04 AC D0 147D 2270 MOVL 4(AP),FP_ADDR(R5) : SET ADDRESS FOR FETCH
      1C A5 08 AC D0 1482 2271 MOVL 8(AP),FP_RETLOC(R5) : AND ADDRESS OF RETURN LOCATION
      50 10 AC D0 1487 2272 MOVL 16(AP),R0 : GET ADDRESS OF CODE SEGMENT
      0C A5 0C AC D0 148B 2273 MOVL 12(AP),ACBSL_PID(R5) : SET TARGET PID
      20 A5 60 80 28 1492 2274 PUSHR #^M<R0,R1,R2,R3,R4,R5> : SAVE REGS FOR MOVC
      3F BA 1497 2275 MOVC3 (R0)+(R0),ACBSL_KAST+8(R5) : COPY CODE SEGMENT TO BUFFER
      52 04 9A 1499 2276 POPR #^M<R0,R1,R2,R3,R4,R5> : RESTORE REGISTERS
      00000000'9F 16 149C 2277 MOVZBL #PRI$ TICOM,R2 : SET PRIORITY INCREMENT CLASS
      04 14A2 2278 JSB @#SCH$QAST : QUEUE AST FOR TARGET
      10$ 14A3 2279 RET : RETURN TO ORIGINAL MODE
      14A3 2280
      0049' 14A3 2281 .SBTTL FPBYTE - FETCH BYTE FROM PROCESS
      14A5 2282 .WORD 90$-2 : SIZE OF CODE SEGMENT
      14A5 2283 IFNORD #1,@FP_ADDR(R5),10$ : BRANCH IF NOT READABLE
      14 AC 2284 MOVB @FP_ADDR(R5),FP_VALUE(R5) : GET VALUE
      10$ 14B1 2285 MOVL FP_ORIGPID(R5),ACBSL_PID(R5) : SET PID FOR RETURN AST
      14B6 2286 MOVB #^X80,ACBSB_RMOD(R5) : SET FOR KAST AGAIN
      14B8 2287 MOVAB B^20$,ACBSL_KAST(R5) : SET NEW AST ADDRESS
      14C0 2288 MOVZBL #PRI$ TICOM,R2 : SET PRIORITY INCREMENT CLASS
      00000000'9F 17 14C3 2289 JMP @#SCH$QAST : QUEUE RETURN AST
      14C9 2290 20$ IFNOWRT #1,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
      14D0 2291 MOVB FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
      14D5 2292 30$ MOVL ACBSL_PID(R5),R1 : GET PID FOR WAKE
      14D9 2293 SETIPL #IPL$-SYNCH : RAISE TO SYNCH
      00000000'9F 16 14DC 2294 JSB @#SCH$WAKE : WAKE PROCESS
      14E2 2295 SETIPL #IPL$-ASTDEL : LOWER IPL
      50 55 D0 14E5 2296 MOVL R5,R0 : SET ADDRESS FOR RELEASE
      00000000'9F 17 14E8 2297 JMP @#EXES$DEANONPAGED : FREE BLOCK AND EXIT
      14EE 2298 90$ : END OF CODE SEGMENT
      14EE 2299
```

		14EE	2301	.SBTTL	DPBYTE - DEPOSIT BYTE TO PROCESS	
0015'		14EE	2302	DPBYTE:	.WORD	90\$-,-2
		14F0	2303	20\$:	IFNOWRT	#1,@FP_RETLOC(R5),30\$
1C B5	14 A5	90	14F7	2304	MOVB	FP_VALUE(R5),@FP_RETLOC(R5)
	50 55	D0	14FC	2305	30\$:	MOV
00000000	'9F	17	14FF	2306	90\$:	MOV
			1505	2307		JMP
			1505	2308		

: SIZE OF CODE SEGMENT  
 : IF NOT WRITABLE THEN SKIP IT  
 : RETURN VALUE  
 : SET ADDRESS FOR RELEASE  
 : FREE BLOCK AND EXIT  
 : END OF CODE SEGMENT

14 A5	14 B5	B0	1505	2310	.SBTTL	FPWORD - FETCH WORD FROM PROCESS
0C A5	10 A5	D0	1505	2311	FPWORD: .WORD	90\$-.-2 ; SIZE OF CODE SEGMENT
0B A5	80 8F	90	1507	2312	IFNORD	#2,@FP_ADDR(R5),10\$ ; BRANCH IF NOT READABLE
18 A5	2B AF	9E	150E	2313	MOVW	@FP_ADDR(R5),FP_VALUE(R5) ; GET VALUE
52 04		9A	1513	2314	10\$: MOVL	FP_ORIGPID(R5),ACBSL_PID(R5) ; SET PID FOR RETURN AST
00000000'9F		17	1518	2315	MOVB	#X80,ACBSB_RMOD(R5) ; SET FOR KAST AGAIN
			151D	2316	MOVAB	B*20\$,ACBSL_KAST(R5) ; SET FOR NEW AST ADDRESS
1C B5	14 A5	B0	1522	2317	MOVZBL	#PRI\$,TICOM,R2 ; SET PRIORITY INCREMENT CLASS
51 0C A5		D0	1525	2318	JMP	@SCH\$QAST ; QUEUE RETURN AST
00000000'9F		16	1528	2319	20\$: IFNOWRT	#2,@FP_RETLOC(R5),30\$ ; IF NOT WRITABLE THEN SKIP IT
			1532	2320	MOVW	FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
50 55		D0	1537	2321	30\$: MOVL	ACBSL_PID(R5),R1 ; GET PID FOR WAKE
00000000'9F		17	153B	2322	SETIPL	#IPL\$,SYNCH ; RAISE TO SYNCH
			153E	2323	JSB	@SCH\$WAKE ; WAKE PROCESS
			1544	2324	SETIPL	#IPL\$,ASTDEL ; LOWER IPL
			1547	2325	MOVL	R5,R0 ; SET ADDRESS FOR RELEASE
			154A	2326	JMP	@EXES\$DEANONPAGED ; FREE BLOCK AND EXIT
			1550	2327	90\$:	END OF CODE SEGMENT
			1550	2328		

DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
DPWORD - DEPOSIT WORD TO PROCESS

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```
0015' 1550 2330 .SBTTL DPWORD - DEPOSIT WORD TO PROCESS
      1550 2331 DPWORD: .WORD 90$-2 ; SIZE OF CODE SEGMENT
      1552 2332 20$: IFNOWRT #2,@FP_RETLOC(R5),30$ ; IF NOT WRITABLE THEN SKIP IT
      1559 2333 MOVW FP_VALUE(R5),@FP_RETLOC(R5) ; RETURN VALUE
      155E 2334 30$: MOVL R5,R0 ; SET ADDRESS FOR RELEASE
      1561 2335 JMP @#EXESDEANONPAGED ; FREE BLOCK AND EXIT
      1567 2336 90$: ; END OF CODE SEGMENT
      1567 2337
```

1C B5 14 A5 80  
50 55 DO  
00000000'9F 17



				1567	2339	.SBTTL	FPLONG - FETCH LONG FROM PROCESS	
			0048'	1567	2340	FPLONG: .WORD	908--2	: SIZE OF CODE SEGMENT
				1569	2341	IFNORD	#4,@FP ADDR(R5),108	: BRANCH IF NOT READABLE
14 A5	14 B5	D0		1570	2342	MOVL	@FP ADDR(R5),FP VALUE(R5)	: GET VALUE
0C A5	10 A5	D0		1575	2343	10\$: MOVL	FP ORIGPID(R5),ACBSL_PID(R5)	: SET PID FOR RETURN AST
0B A5	80 BF	90		157A	2344	MOV8	#*X80,ACBSB_RMOD(R5)	: SET FOR KAST AGAIN
18 A5	BC AF	9E		157F	2345	MOVAB	B*208,ACBSL_KAST(R5)	: SET NEW KAST ADDRESS
	52	D4		1584	2346	CLRL	R2	: NULL PRIO INCR
	00000000'9F	17		1586	2347	JMP	@SCHSQAST	: QUEUE RETURN AST
				158C	2348	20\$: IFNOWRT	#4,@FP RETLOC(R5),308	: IF NOT WRITABLE THEN SKIP IT
1C B5	14 A5	D0		1593	2349	MOVL	FP VALUE(R5),@FP_RETLOC(R5)	: RETURN VALUE
51	0C A5	D0		1598	2350	30\$: MOVL	ACBSL_PID(R5),R1	: GET PID FOR WAKE
				159C	2351	SETIPL	#IPL\$-SYNCH	: RAISE TO SYNCH
	00000000'9F	16		159F	2352	JSB	@SCH\$WAKE	: WAKE PROCESS
	50	D0		15A5	2353	SETIPL	#IPL\$-ASTDEL	: LOWER IPL
	00000000'9F	17		15A8	2354	MOVL	R5,R0	: SET ADDRESS FOR RELEASE
				15AB	2355	JMP	@EXESDEANONPAGED	: FREE BLOCK AND EXIT
				15B1	2356	90\$:		: END OF CODE SEGMENT
				15B1	2357			

DELTA  
V04-000

- MULTIMODE PROCESS DEBUGGER  
DPLONG - DEPOSIT LONGWORD TO PROCESS

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```
0015' 15B1 2359 .SBTTL DPLONG - DEPOSIT LONGWORD TO PROCESS
      15B1 2360 DPLONG: .WORD 908-2 : SIZE OF CODE SEGMENT
      15B3 2361 20$: IFNOWRT #4,@FP_RETLOC(R5),30$ : IF NOT WRITABLE THEN SKIP IT
      15BA 2362 : MOVL FP_VALUE(R5),@FP_RETLOC(R5) : RETURN VALUE
      15BF 2363 30$: MOVL R5,R0 : SET ADDRESS FOR RELEASE
      15C2 2364 : JMP @#EXESDEANONPAGED : FREE BLOCK AND EXIT
      15C8 2365 90$: : END OF CODE SEGMENT
      15C8 2366 DELEND: :
      15C8 2367 : .ENDC :
      15C8 1 : .END TEST : DECLARE START ADDRESS
```

1C B5 14 A5 DO  
50 55 DO  
00000000'9F 17

DELTA  
Symbol table

- MULTIMODE PROCESS DEBUGGER

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\$\$T1	= 00000000		
ACBSB_RMOD	= 00000008		
ACBSL_AST	= 00000010		
ACBSL_ASTPRM	= 00000014		
ACBSL_KAST	= 00000018		
ACBSL_PID	= 0000000C		
ADD	00000606	R	02
ASTEN	0000000E	R	02
B	0000008C	R	02
BLANK	00000A55	R	02
BMSG	00000D96	R	02
BRKADR	= 0000039C	R	02
BRKCOM	= 000003E9	R	02
BRKDSP	= 000003C9	R	02
BRKOP	= 000003C3	R	02
BRKPOINT	00000B95	R	02
BSLSH	= 0000005C		
CATCHALL	00001263	R	02
CLISV_DBGEXCP	= 00000010		
CLREXV_EXEC	0000131C	R	02
CLREXV_KERNEL	000012FA	R	02
COLON	00000C58	R	02
COMMA	00000666	R	02
CONTEXT	0000000C	R	02
CONTEXTSZ	= 000000E4		
CR	= 0000000D		
CRLF	00000991	R	02
CTL\$GL_USRUNDWN	*****W	GX	02
CURDOT	0000008C	R	02
CURTYPE	0000008A	R	02
DBGACTIVE	000004D4	R	02
DBGEXCEP	00001241	R	02
DBGINPUT	0000047B	R	02
DCOM	0000054D	R	02
DELBASE	00000000	R	02
DELEND	000015C8	R	02
DELTA_START	00000FC1	R	02
DEPOSTT	00000EE3	R	02
DEPPREG	00000F72	R	02
DIV	00000602	R	02
DOT	00000C68	R	02
DPBYTE	000014EE	R	02
DPLONG	000015B1	R	02
DPWORD	00001550	R	02
DQUOTE	0000060A	R	02
DTYPE	00000089	R	02
ECOND_LASTCHANC	000004FC	R	02
ECOND_PRIMARY	000004F0	R	02
ENDEXPR	000005DB	R	02
ENDFIELD	00000669	R	02
EQL1	00000AC4	R	02
EQUALS	00000ABD	R	02
ERR2	00000B00	R	02
ERR3	00000CE8	R	02
ERR4	00000645	R	02
ERROR	00000556	R	02
ESCAP	00000A7A	R	02

EXCEPT	000011CD	R	02
EXCMG	00001290	R	02
EXCODA	000004E4	R	02
EXESALLOCBUF	*****W	GX	02
EXESDEANONPAGED	*****W	GX	02
EXECUTE	00000F95	R	02
EXIHADR	000004DC	R	02
EXIHANDLE	0000129C	R	02
EXIMSG	000012F1	R	02
EXITBLK	000004D8	R	02
EXITCODE	000004E8	R	02
F1	00000064	R	02
F2	00000068	R	02
F3	0000006C	R	02
F4	00000070	R	02
F5	00000074	R	02
FCTR	00000088	R	02
FETCH	00000684	R	02
FETCHP	000013FB	R	02
FPBYTE	000014A3	R	02
FPLONG	00001567	R	02
FPWORD	00001505	R	02
FP_ADDR	= 00000014		
FP_ORIGPID	= 00000010		
FP_RETLOC	= 0000001C		
FP_VALUE	= 00000014		
FTCHPREG	0000068A	R	02
GETBPTX	00000EBF	R	02
GETCHAR	0000099B	R	02
GETCMD	00000E14	R	02
GLOBL	000005C6	R	02
GO	00000C47	R	02
HIGH	000005CC	R	02
IAC\$AW_VECSET	*****W	X	02
INBUF	00000010	R	02
INFLD	000005C2	R	02
INIBRKA	000003A0	R	02
INITCALL	0000100B	R	02
INSBUF	00000084	R	02
INSLEN	00000080	R	02
INSTR	00000A90	R	02
IOSM_EXTEND	= 00008000		
IOS_READVBLK	= 00000031		
IOS_WRITEVBLK	= 00000030		
IPLS_ASTDEL	= 00000002		
IPLS_SYNCH	= 00000008		
IRPSC_LENGTH	= 000000C4		
KCOND_LASTCHANC	000004F8	R	02
KCOND_PRIMARY	000004EC	R	02
LBRACKET	00000B08	R	02
LF	= 0000000A		
LIB\$INS_DECODE	*****W	GX	02
LINEFEED	000006E5	R	02
LOCOUT	000006ED	R	02
LOCP	00000ABD	R	02
LOCPROMPT	000006EA	R	02
MCHK	00000CE8	R	02

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Symbol table

- MULTIMODE PROCESS DEBUGGER

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MFYFLG	00000078	R	02	PSL\$C_USER	= 00000003		
MFYFLGS	000000C62	R R	02	PSL\$S_CURMOD	= 000000002		
MODES	000000803	R R	02	PSL\$S_PVRMOD	= 000000002		
MUL	0000005FE	R	02	PSL\$V_CURMOD	= 000000018		
NBRK	= 000000008			PSL\$V_PVRMOD	= 000000016		
NEGATE	000000A5E	R R	02	PSL\$V_TBIT	= 000000004		
NEXTDOT	0000006C9	R R R	02	QGET	0000143F	R	02
NEXTLOC	0000006E8	R R R	02	QUAN	000000090	R R R	02
NEXTP	00000056D	R	02	QUANT	000000C75	R	02
NMODES	= 000000005			QUOT	= 000000027		
NOBRK	00001004	R	02	QUOTE	000000ECF	R	02
NPRIM	= 00000002C			RDBUF	= 000000002		
NSEC	= 000000007			RDCR	= 000000000		
NTERM	= 00000000A			REGCOM	000000C95	R R R	02
NTMPBRK	= 000000001			REGISTER	000000C8D	R R R	02
OPEN	000000614	R R	02	RELOC	00000081E	R R R	02
OPER	00000008B	R R	02	REPRUT	000013FA	R R R	02
OPERATOR	000000A55			RESET	000000ACC	R R R	02
OPERBAS	= 000000012			RESTORE	000000D4A	R R R	02
OUTB	= 000000006			RESTORR	000000D58	R R R	02
OUTBB	0000006E2	R R	02	RETURN	000000648	R R R	02
OUTBSLSM	000000939	R R R	02	RSET	00000065C	R	02
OUTBUF	000000094	R R R	02	RUBOUT	= 00000007F		
OUTCHAR	000000942	R R R	02	SALUTE	000000FA8	R	02
OUTCOM	0000008CB	R	02	SAV...	= 00000039C	R R R	02
OUTCR	= 000000004			SAVAP	0000000D4	R R R	02
OUTDIGIT	0000008C4	R R	02	SAVE	000000CEB	R R R	02
OUTER	000000545	R R R	02	SAVOCR	0000000E8	R R R	02
OUTINS	000000722	R R R	02	SAVPC	0000000E0	R R R	02
OUTLONG	0000008C8	R R R	02	SAVPSL	0000000E4	R R R	02
OUTPC	000000E01	R R	02	SAVR2	0000000AC	R R R	02
OUTPUT	0000006F6	R R	02	SAVRCR	0000000EA	R R R	02
OUTPUTA	00000084E	R R R	02	SAVREG	0000000A4	R R R	02
OUTPUT_ADDRESS	00000079D	R R R	02	SAVRXCS	0000000EC	R R R	02
OUTRB	00000093F	R R R	02	SAVSP	0000000DC	R R R	02
OUTSPACE	00000098C	R R	02	SCANP	000000569	R	02
OUTZBUF	0000008E1	R R	02	SCH\$GL_MAXPIX	*****W	GX	02
OUTZSTRING	0000008E5	R	02	SCH\$QAST	*****W	GX	02
OVEROPCLEN	= 000000005			SCH\$WAKE	*****W	GX	02
OVEROPCODES	000000514	R R	02	SCOND_LASTCHANC	000000500	R	02
OVRADR	0000003C0	R R	02	SCOND_PRIMARY	0000004F4	R R R	02
OVROPC	00000039C	R	02	SECOND	000000AD9	R R R	02
PCBSL_PID	= 000000060			SEMI	000000AE0	R R R	02
PID	00000007C	R	02	SETBRK	000000E89	R R R	02
PR\$ IPL	= 000000012			SETTEXC	00001174	R R R	02
PRET	0000005D3	R R	02	SETKEXC	0000113F	R R R	02
PREG	000000FA3	R R	02	SETPRTK	000013E2	R R R	02
PREXC	000000F81	R	02	SETRUNDWN	00001361	R R R	02
PRIS TICOM	= 000000004			SETWRT	000013C3	R R R	02
PRIMARY	000000519	R R R	02	SHFT	0000005F9	R R R	02
PROCD	000000C50	R R R	02	SHOBRK	000000C05	R R R	02
PROCEED	000000E19	R R R	02	SLASH	00000060F	R	02
PROGCTR	000000C7B	R	02	SLSH	= 00000002F		
PRT\$C_UW	= 000000004			SPACES	00000071E	R	02
PSL\$C_EXEC	= 000000001			SS\$ BREAK	= 000000414		
PSL\$C_KERNEL	= 000000000			SS\$ COMPAT	= 00000042C		
PSL\$C_SUPER	= 000000002			SS\$ DEBUG	= 00000046C		



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Symbol table

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SS\$EXQUOTA	= 0000001C		
SS\$EXQUOTAEND	= 00002AFF		
SS\$EXQUOTAEND	= 00002A00		
SS\$INSFMEM	= 00000124		
SS\$NONEXPR	= 000008E8		
SS\$NOPRIV	= 00000024		
SS\$NORMAL	= 00000001		
SS\$NOTRAN	= 00000629		
SS\$TBIT	= 00000464		
SS\$UNWINDING	= 00000928		
SS\$VECFULL	= 00002034		
STATUS	00000060	R	02
STEP	00000B37	R R	02
STEPOVER	00000B44	R R	02
SUPERST	0000055D	R	02
SW PROCESS	= 00000001		
SYSS\$ASSIGN	*****	GX	02
SYSS\$CMEXEC	*****	GX	02
SYSS\$CMKRN	*****	GX	02
SYSS\$DCLEXH	*****	GX	02
SYSS\$EXIT	*****	GX	02
SYSS\$HIBER	*****	GX	02
SYSS\$QIOW	*****	GX	02
SYSS\$SETAST	*****	GX	02
SYSS\$SETEXV	*****	GX	02
SYSS\$SETPRT	*****	GX	02
SYSS\$TRNLOG	*****	GX	02
SYSS\$WAITFR	*****	GX	02
SYSS\$WAKE	*****	GX	02
TAB	00000A6A	R	02
TERM	00000532	R R	02
TERMASK	00000504	R R	02
TERMASKADR	00000473	R	02
TERMASKLEN	= 00000010		
TEST	00000FBF	R	02
TRMSM TM NOEDIT	= 00008000		
TRMS\$MODIFIERS	= 00000000		
TRMS\$TERM	= 00000003		
TRNINPUT	0000048C	R	02
TTCHAN	00000455	R R	02
TTIOSB	0000044D	R R	02
TTITMLST	00000463	R	02
TTITMLSTLEN	= 00000018		
TTNAMD	00000459	R R	02
UNBRK	00000E61	R R R	02
VALI	00000C8A	R R R	02
VALR	00000C87	R R R	02
VALUE	00000C7F	R	02
V_ASCII	= 00000001		
V_ATBRK	= 00000004		
V_F1	= 00000008		
V_F2	= 00000009		
V_F3	= 0000000A		
V_F4	= 0000000B		
V_F5	= 0000000C		
V_INFIELD	= 00000002		
V_INSTR	= 0000000D		

V\_NEGATE  
V\_OPEN  
V\_PREG  
V\_PRMODE  
V\_RUB  
V\_TBIT  
V\_TBITOK  
XDELACV  
XDELBPT  
XDELDBG  
XDELTBIT  
XDT\$START  
XREG  
XREGV  
XSET

= 00000007		
= 00000000		
= 0000001F		
= 0000000F		
= 00000006		
= 00000003		
= 00000005		
00000CE8	R	02
00000DA0	R	02
00000E55	R	02
00000E48	R	02
00000FBF	RG	02
00000CDC	R	02
0000040D	R	02
00000CCA	R	02



+-----+  
! Psect synopsis !  
+-----+

PSECT name	Allocation	PSECT No.	Attributes
ABS	00000000 ( 0.)	00 ( 0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000 ( 0.)	01 ( 1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
Z\$DEBUG_CODE	000015C8 ( 5576.)	02 ( 2.)	PIC USR CON REL LCL NOSHR EXE RD WRT NOVEC LONG

+-----+  
! Performance indicators !  
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.06	00:00:01.27
Command processing	148	00:00:00.66	00:00:06.91
Pass 1	536	00:00:15.18	00:01:09.72
Symbol table sort	0	00:00:02.08	00:00:09.06
Pass 2	406	00:00:04.49	00:00:21.91
Symbol table output	1	00:00:00.17	00:00:00.80
Psect synopsis output	0	00:00:00.02	00:00:00.31
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	1122	00:00:22.66	00:01:49.98

The working set limit was 2250 pages.  
129975 bytes (254 pages) of virtual memory were used to buffer the intermediate code.  
There were 100 pages of symbol table space allocated to hold 1725 non-local and 181 local symbols.  
2369 source lines were read in Pass 1, producing 26 object records in Pass 2.  
41 pages of virtual memory were used to define 40 macros.

+-----+  
! Macro library statistics !  
+-----+

Macro library name	Macros defined
\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	10
\$255\$DUA28:[SYSLIB]STARLET.MLB;2	27
TOTALS (all libraries)	37

1738 GETS were required to define 37 macros.

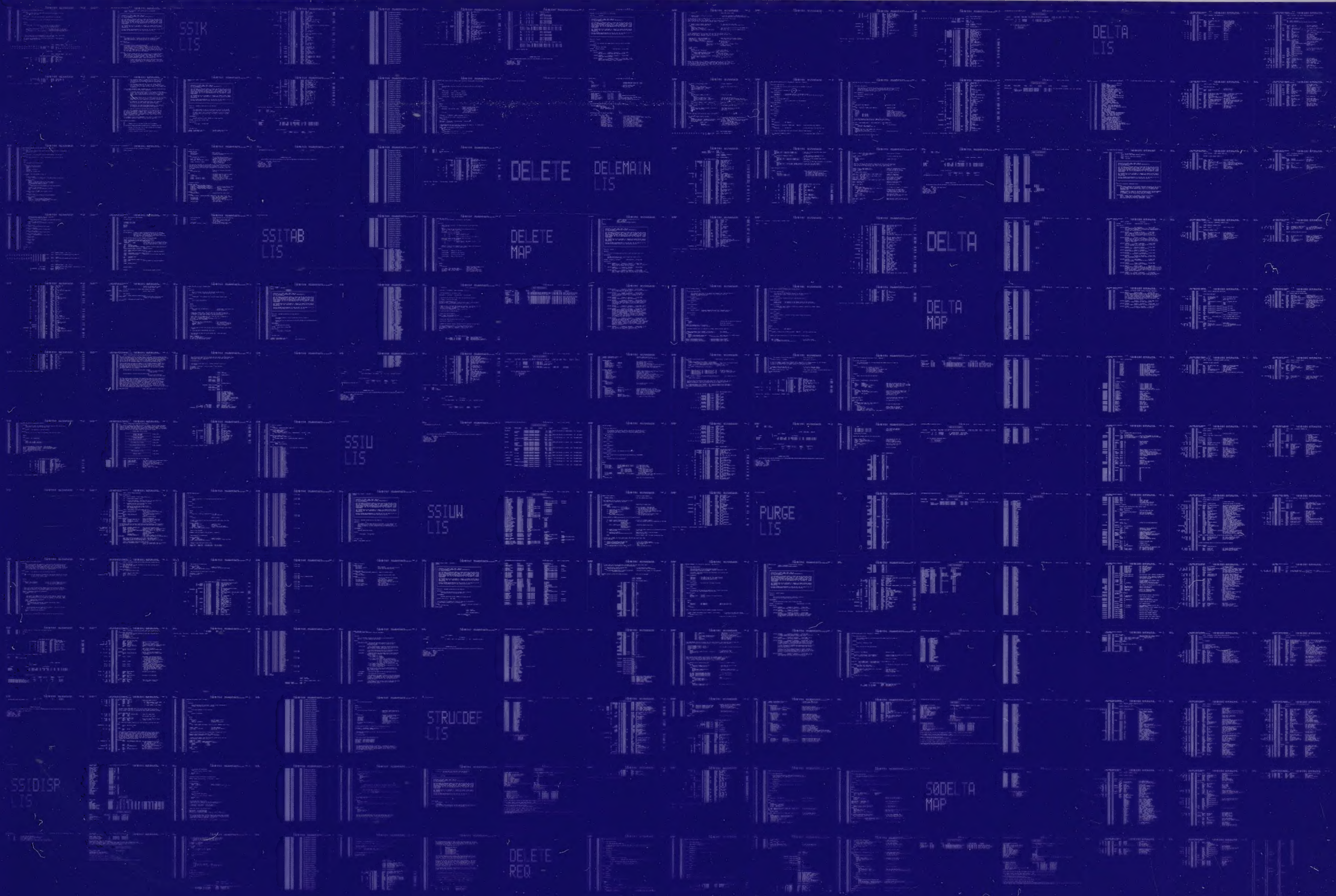
There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:DELTA/OBJ=OBJ\$:DELTA MSRC\$:SWP/UPDATE=(ENH\$:SWP)+MSRC\$:XDELTA/UPDATE=(ENH\$:XDELTA)+MSRC\$:ENDP/UPDATE=(ENH\$:ENDP)+EXEC



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